General Description

The MAX3187 complete, dual DTE RS-232 serial port meets the stringent ESD requirements of the European Community. All transmitter outputs and receiver inputs are protected to \pm 15kV using IEC 1000-4-2 Air-Gap Discharge, \pm 8kV using IEC 1000-4-2 Contact Discharge, and \pm 15kV using the Human Body Model.

The MAX3187's six RS-232 transmitters and ten RS-232 receivers require no charge pump. Guaranteed to run at data rates up to 230kbps, the MAX3187 is optimized for desktop PC and motherboard applications, and is compatible with popular software for PC communications. Power-supply current is less than 1mA for IDD and ISS, and less than 3mA for ICC.

The MAX3187 is available in a space-saving 36-pin SSOP package.

Applications

Desktop PC Motherboards Instruments

Equipment Meeting IEC 1000-4-2

_Features

- Enhanced ESD Protection: ±15kV—Human Body Model ±8kV—IEC 1000-4-2, Contact Discharge ±15kV—IEC 1000-4-2, Air-Gap Discharge
- Latchup Free During an ESD Event
- ✤ 36-Pin SSOP Package
- Guaranteed 230kbps Data Rate
- Flow-Through Pinout
- Two Complete DTE Serial Ports
- ♦ 6 RS-232 Drivers and 10 RS-232 Receivers

_Ordering Information

MXXM

PART	TEMP. RANGE	PIN-PACKAGE
MAX3187CAX	0°C to +70°C	36 SSOP
MAX3187EAX	-40°C to +85°C	36 SSOP

Typical Operating Circuit appears at end of data sheet.

_Pin Configuration



M/X/W

_ Maxim Integrated Products 1

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ABSOLUTE MAXIMUM RATINGS

V _{CC}	-0.3V to +7V
V _{DD}	
V _{SS}	+0.3V to -14V
Input Voltages	
T_IN	
R_IN	±30V
Output Voltages	
T_OUT	±15V
R_OUT	0.3V to (V _{CC} + 0.3V)

Short-Circuit Duration
T_OUT (one at a time)Continuous
R_OUT (one at a time)Continuous
Continuous Power Dissipation ($T_A = +70^{\circ}C$)
SSOP (derate 11.76mW/°C above +70°C)762mW
Operating Temperature Ranges
MAX3187CAX0°C to +70°C
MAX3187EAX40°C to +85°C
Storage Temperature Range65°C to +160°C
Lead Temperature (soldering, 10sec)+300°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

(V_{CC} = +4.5V to +5.5V, V_{DD} = +10.8V to +13.2V, V_{SS} = -10.8V to -13.2V, T_A = T_{MIN} to T_{MAX}, unless otherwise noted. Typical values are at T_A = +25°C.)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
DC CHARACTERISTICS			L			
	Vcc		4.5		5.5	
Operating Voltage Range	Vdd		10.8		13.2	V
	Vss		-13.2		-10.8	
	I _{CC} No load		750	3000		
Supply Current	IDD	No load		370	1000	μA
	ISS	No load		370	1000	
LOGIC						r
Input Logic Threshold Low	VILT	T_IN			0.8	V
Input Logic Threshold High	VIHT	T_IN	2.0			V
Input Leakage Current				0.01	1	μA
Output Voltage Low	Volr	R_OUT ; $I_{SINK} = 3.2mA$			0.4	V
Output Voltage High	Vohr	R_OUT; I _{SOURCE} = 1mA	V _{CC} - 0.6			V
TRANSMITTER OUTPUTS			L			
Output Voltage Swing		V_{DD} = 7.0V, V_{SS} = -7.0V, R_L = 3k Ω	±5.0			- V
Output Voltage Swing		V_{DD} = 12V, V_{SS} = -12V, R_L = 3k Ω	±9.5			
Transmitter Output Resistance		$V_{CC} = V_{DD} = V_{SS} = 0, V_{T_{OUT}} = \pm 2V$	300			Ω
RS-232 Output Short-Circuit Current				±35	±60	mA
RECEIVER INPUTS		I				
Receiver Input Voltage Range			-25		25	V
RS-232 Input Threshold Low			0.75			V
RS-232 Input Threshold High					2.4	V
RS-232 Input Hysteresis				0.65		V
RS-232 Input Resistance			3	5	7	kΩ
Receiver Output Short-Circuit Current				±10		mA

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ELECTRICAL CHARACTERISTICS (continued)

 $(V_{CC} = +4.5V \text{ to } +5.5V, V_{DD} = +10.8V \text{ to } +13.2V, V_{SS} = -10.8V \text{ to } -13.2V, T_A = T_{MIN} \text{ to } T_{MAX}$, unless otherwise noted. Typical values are at $T_A = +25^{\circ}$ C.)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS	
ESD CHARACTERISTICS								
		Human Body Model			±15			
ESD Protection		IEC 1000-4-2 (Contact Discharge)			±8		kV	
		IEC 1000-4-2 (Air-Gap Discharge)			±15			
TIMING CHARACTERISTICS								
Transmitter Output Propagation Delay, Low to High	t PLHT					4	μs	
Transmitter Output Propagation Delay, High to Low	t _{PHLT}	$V_{DD} = 12V, V_{SS} = -12V, R_L = 3k\Omega, C_L = 1000pF, T_A = +25^{\circ}C$				4	μs	
Transmitter Propagation Delay Skew, tpLHT - tpHLT	tskt	$V_{DD} = 12V, V_{SS} = -12V, R_L = 3k\Omega, C_L = 1000pF, T_A = +25^{\circ}C$				0.4	μs	
Transition Output Slew Rate	SR	$\label{eq:VDD} \begin{array}{l} V_{DD} = 12V, V_{SS} = -12V, \\ R_L = 3k\boldsymbol{\Omega} \mbox{ to } 7k\boldsymbol{\Omega}, \\ measured \mbox{ from } + 3V \mbox{ to } -3V \\ or \mbox{ or } -3V \mbox{ to } + 3V, \\ Figure \mbox{ 1, } T_A = +25^{\circ}C \end{array}$	C _L = 150pF to 2500pF	4		30	- V/µs	
			C _L = 50pF to 1000pF	8		30		
Receiver Output Propagation Delay, Low to High	t _{PLHR}	$V_{CC} = 5V, C_L = 50pF, T_A = +25^{\circ}C$				4.0	μs	
Receiver Output Propagation Delay, High to Low	t _{PHLR}	$V_{CC} = 5V, C_L = 50pF, T_A = +25^{\circ}C$				4.0	μs	
Receiver Propagation Delay Skew, tpLHR - tpHLR	tskr	V _{CC} = 5V, C _L = 50pF, T _A = +25°C				0.4	μs	
Guaranteed Data Rate	DR	$V_{SS} = -12V,$ $R_{L} = 3k\Omega \text{ to } 7k\Omega,$ $T_{A} = +25^{\circ}C, \text{ any two}$	C _L = 50pF to 2500pF	120			- kbps	
			C _L = 50pF to 1000pF	230			, vnh2	
Mouse Driveability		T3OUT = -5mA, T1OUT = T2OUT = 5mA		±7.5			V	

Typical Operating Characteristics

 $(V_{CC} = +5V, V_{DD} = +12V, V_{SS} = -12V, T_A = +25^{\circ}C, unless otherwise noted.)$



TRANSMITTER OUTPUT VOLTAGE vs. SLEW RATE TRANSMITTER OUTPUT VOLTAGE vs. LOAD CAPACITANCE LOAD CURRENT PER TRANSMITTER vs. TEMPERATURE 15 12.5 25 120kbps 10.0 TRANSMITTER OUTPUT VOLTAGE (V) **FRANSMITTER OUTPUT VOLTAGE (V)** 10 240kbps 7.5 20 V_{OUT+} 5.0 2 TRANSMITTER, 240kbps 5 SLEW RATE (V/ms) FALL, 500pF 2.5 4 TRANSMITTERS, 15kbps 15 $3k\Omega + C_{LOAD}$ 0 0 RISE, 500pF -2.5 10 V_{OUT}. -5 -5.0 240kbps RISE, 2000pF -7.5 5 FALL, 2000pF -10 120kbps -10.0 -12.5 -15 0 0 1000 2000 3000 4000 5000 0 2 4 6 8 10 12 14 16 18 -55 -35 -15 5 25 45 65 85 105 125 LOAD CAPACITANCE (pF) LOAD CURRENT PER TRANSMITTER (mA) TEMPERATURE (°C)

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_Pin Description

PIN	NAME	FUNCTION	
1, 2, 3, 6, 8, 11, 12, 13, 16, 18	R1OUT-R10OUT	Receiver Outputs, swing between GND and VCC	
4, 5, 7, 14, 15, 17	T1IN-T6IN	Transmitter Inputs	
9	V _{CC}	Supply-Voltage Input, 4.5V to 5.5V	
10	Vss	Supply-Voltage Input, -10.8V to -13.2V	
27	GND	Ground. Connect system to ground.	
28	V _{DD}	Supply-Voltage Input, 10.8V to 13.2V	
19, 21, 24, 25, 26, 29, 31, 34, 35, 36	R10IN-R1IN	Receiver Inputs	
20, 22, 23, 30, 32, 33	T6OUT-T1OUT	Transmitter Outputs, swing between V_{DD} and V_{SS}	



Figure 1. Slew-Rate Test Circuit and Timing Diagram

Detailed Description

±15kV ESD Protection

As with all Maxim devices, ESD-protection structures are incorporated on all pins to protect against electrostatic discharges (ESD) encountered during handling and assembly. The MAX3187 driver outputs and receiver inputs have extra protection against static electricity found in normal operation. Maxim's engineers developed state-of-the-art structures to protect these pins against ±15kV ESD, without damage. After an ESD event, the MAX3187 continues working without latchup.

ESD protection can be tested in several ways. The transmitter outputs and receiver inputs are characterized for protection to the following:

- 1) ±15kV using the Human Body Model
- 2) ±8kV using the Contact-Discharge Method specified in IEC 1000-4-2 (formerly IEC 801-2)
- ±15kV using the Air-Gap Method specified in IEC 1000-4-2 (formerly IEC 801-2)

ESD Test Conditions

ESD performance depends on a number of conditions. Contact Maxim for a reliability report that documents test setup, methodology, and results.



Figure 2a. Human Body ESD Test Model



Figure 3a. IEC 1000-4-2 ESD Test Model

Human Body Model

Figure 2a shows the Human Body Model, and Figure 2b shows the current waveform it generates when discharged into a low impedance. This model consists of a 100pF capacitor charged to the ESD voltage of interest, which is then discharged into the device through a $1.5k\Omega$ resistor.

IEC 1000-4-2

Since January 1996, all equipment manufactured and/or sold in the European community has been required to meet the stringent IEC 1000-4-2 specification. The IEC 1000-4-2 standard covers ESD testing and performance of finished equipment; it does not specifically refer to integrated circuits. The MAX3187



Figure 2b. Human Body Model Current Waveform



Figure 3b. IEC 1000-4-2 ESD-Generator Current Waveform

helps you design equipment that meets Level 4 (the highest level) of IEC 1000-4-2, without additional ESD-protection components.

The main difference between tests done using the Human Body Model and IEC 1000-4-2 is higher peak current in IEC 1000-4-2. Because series resistance is lower in the IEC 1000-4-2 ESD test model (Figure 3a), the ESD withstand voltage measured to this standard is generally lower than that measured using the Human Body Model. Figure 3b shows the current waveform for the \pm 8kV IEC 1000-4-2 Level 4 ESD Contact-Discharge test.

The Air-Gap test involves approaching the device with a charge probe. The Contact-Discharge method connects the probe to the device before the probe is energized.



Machine Model

The Machine Model for ESD testing uses a 200pF storage capacitor and zero-discharge resistance. It mimics the stress caused by handling during manufacturing and assembly. Of course, all pins (not just RS-232 inputs and outputs) require this protection during manufacturing. Therefore, the Machine Model is less relevant to the I/O ports than are the Human Body Model and IEC 1000-4-2.

_Applications Information

Use proper layout to ensure other devices on your board are not damaged in an ESD strike. Currents as high as 60A can instantaneously pass into ground, so be sure to minimize the ground-lead return path to the power supply. A separate return path to the power supply is recommend. Trace widths should be greater than 40 mils. Bypass V_{CC}, V_{DD}, and V_{SS} with 0.1µF capacitors as close to the part as possible to ensure maximum ESD protection.

Connect any unused transmitter inputs to GND or V_{CC}. The MAX3187 is not sensitive to power-supply sequencing, and therefore requires no external protection diodes.

Chip Information

TRANSISTOR COUNT: 419

__Typical Operating Circuit



MAX3187



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