

# 7MBR75VP060-50

**IGBT Modules** 

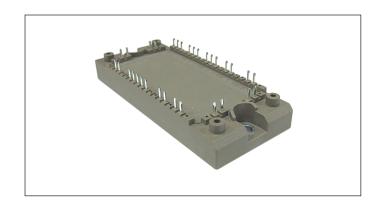
# IGBT MODULE (V series) 600V / 75A / PIM

## **■** Features

Low V<sub>CE</sub>(sat)
Compact Package
P.C.Board Mount Module
Converter Diode Bridge Dynamic Brake Circuit
RoHS compliant product

# ■ Applications

Inverter for Motor Drive AC and DC Servo Drive Amplifier Uninterruptible Power Supply



# ■ Maximum Ratings and Characteristics

● Absolute Maximum Ratings (at Tc=25°C unless otherwise specified)

Items		Symbols	Conditions		Maximum ratings	Units	
Collector-Emit	ter voltage	Vces			600	V	
Gate-Emitter v	oltage	V <sub>GES</sub>			±20	V	
5	Collector current	Ic	Continuous	Tc=80°C	75		
Collector curre		Icp	1ms	Tc=80°C	150	Α	
Collector curre		-lc			75	А	
	-lc pulse	1ms		150			
Collector power dissipation		Pc	1 device		300	W	
Collector-Emit	Collector-Emitter voltage				600	V	
Gate-Emitter v	Gate-Emitter voltage				±20	V	
Callanton commant	Ic	Continuous	Tc=80°C	50	۸		
Collector current		Іср	1ms	Tc=80°C	100	Α	
Collector power dissipation		Pc	1 device		215	W	
Repetitive pea	Repetitive peak reverse voltage (Diode)				600	V	
Repetitive pea	Repetitive peak reverse voltage					V	
Average output Surge current	Average output current		50Hz/60Hz, sine wave		75	Α	
Surge current	Surge current (Non-Repetitive)		10ms, Tj=150°C half sine wave		500	Α	
	I²t (Non-Repetitive)				1250	A <sup>2</sup> s	
Junction temperature		Tj Inverter, Brake Converter		175			
					150	°C	
Operating junciton temperature (under switching conditions)		Tjop Inverter, Brake Converter		;	150		
				150	C		
Case temperature		Tc			125		
Storage temperature		Tstg					
olation voltage between terminal and copper base (*1) between thermistor and others (*2)		Viso	AC : 1min.		2500	VAC	
crew torque Mounting (*3)		-	M5		3.5	N m	

Note \*1: All terminals should be connected together during the test.

Note \*2: Two thermistor terminals should be connected together, other terminals should be connected together and shorted to base plate during the test.

Note \*3: Recommendable value: 2.5-3.5 Nm (M5)

# ● Electrical characteristics (at Tj= 25°C unless otherwise specified)

Items		Symbols	Conditions		Characteristics			Units	
		Syllibols			min.	typ. max.		Units	
	Zero gate voltage collector current	Ices	V <sub>GE</sub> = 0V, V <sub>CE</sub> = 600V		-	-	1.0	mA	
_	Gate-Emitter leakage current	Iges	V <sub>GE</sub> = 0V, V <sub>GE</sub> = ±20V		-	-	200	nA	
	Gate-Emitter threshold voltage	V <sub>GE (th)</sub>	V <sub>CE</sub> = 20V, I <sub>C</sub> = 75mA		6.2	6.7	7.2	V	
		V	V <sub>GE</sub> = 15V I <sub>C</sub> = 75A	Tj=25°C	-	1.90	2.35		
		V <sub>CE (sat)</sub> (terminal)		Tj=125°C	-	2.20	-		
	Collector-Emitter saturation voltage	(terriiriar)		Tj=150°C	-	2.30	-	V	
		V <sub>CE (sat)</sub> (chip)	V <sub>GE</sub> = 15V I <sub>C</sub> = 75A	Tj=25°C	-	1.60	2.05	v	
				Tj=125°C	-	1.90	-		
		(6þ)		Tj=150°C	-	2.00	-		
	Input capacitance	Cies	$V_{CE} = 10V$ , $V_{GE} = 0V$ , $f = 1MHz$		-	4.9	-	nF	
rte	Turn-on time	ton			-	0.36	1.20		
Inverter		tr	Vcc = 300V		-	0.25	0.60		
		tr (i)	Ic = 75A V <sub>GE</sub> = +15 / -15V		-	0.07	-	μs	
	T 55 Al	toff	$R_G = 30\Omega$		-	0.52	1.20		
	Turn-off time	tf			-	0.03	0.45		
				Tj=25°C	-	1.90	2.35	V	
	Forward on voltage	V <sub>F</sub> (terminal)	I <sub>F</sub> = 75A	Tj=125°C	-	1.80	-		
		(terrillial)		Tj=150°C	-	1.75	-		
			I <sub>F</sub> = 75A	Tj=25°C	-	1.60	2.05		
		V <sub>F</sub>		Tj=125°C	-	1.50	-		
		(chip)		Tj=150°C	-	1.45	-		
	Reverse recovery time	trr	I <sub>F</sub> = 75A		-	-	0.35	μs	
	Zero gate voltage collector current	Ices	V <sub>GE</sub> = 0V V <sub>CE</sub> = 600V		-	-	1.0	mA	
	Gate-Emitter leakage current	Iges	V <sub>CE</sub> = 0V V <sub>GE</sub> = +20 / -20V		-	-	200	nA	
0	Collector-Emitter saturation voltage	V <sub>CE (sat)</sub> (terminal)	V <sub>GE</sub> = 15V	Tj=25°C	-	1.80	2.25	V	
				Tj=125°C	-	2.10	-		
			Ic = 50A	Tj=150°C	-	2.20	-		
Brake			V <sub>GE</sub> = 15V I <sub>C</sub> = 50A	Tj=25°C	-	1.60	2.05		
Ā		V <sub>CE</sub> (sat)		Tj=125°C	-	1.90	-		
		(chip)		Tj=150°C	-	2.00	-		
	Turn-on time	ton	V <sub>CE</sub> = 300V		-	0.36	1.20		
		tr	Ic = 50A		-	0.25	0.60	μs	
	Turn-off time	toff	V <sub>GE</sub> = +15 / -15V		-	0.52	1.20		
		tf	$R_G = 43\Omega$		-	0.03	0.45		
	Reverse current	IRRM	V <sub>R</sub> = 600V		-	-	1.00	mA	
e.	Forward on voltage	V <sub>FM</sub> (chip)	I <sub>F</sub> = 75A	terminal	-	1.55	2.00		
Converte				chip	-	1.25	-	V	
Con	Reverse current	IRRM	V <sub>R</sub> = 800V		-	-	1.0	mA	
lor			T = 25°C T = 100°C		-	5000	-	Ω	
Thermistor	Resistance	R			465	495	520		
The	B value B T = 25 / 50°C			3305	3375	3450	K		

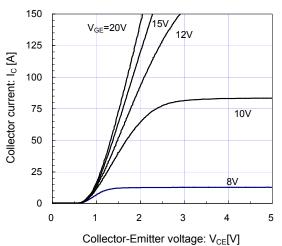
#### Thermal resistance characteristics

Items	Symbols	Conditions	Characteristics			Units
items		Conditions	min.	typ.	max.	Ullits
	Rth(j-c)	Inverter IGBT	-	-	0.50	°C/W
Thermal resistance (1device)		Inverter FWD	-	-	0.95	
Thermal resistance (Tuevice)		Brake IGBT	-	-	0.71	
		Converter Diode	-	-	0.82	
Contact thermal resistance (1device) (*4) Rth(c-f)		with Thermal Compound	-	0.05	-	

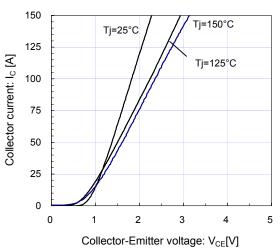
Note  $^{\star}4$ : This is the value which is defined mounting on the additional cooling fin with thermal compound.

#### ■ Characteristics (Representative)

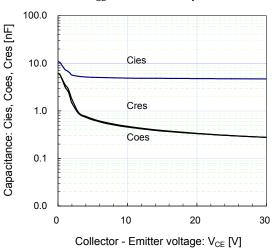
[ Inverter ] Collector current vs. Collector-Emitter voltage (typ.)  $Tj=25^{\circ}C$  / chip



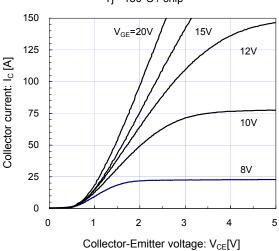
 $[Inverter] \\ Collector current vs. Collector-Emitter voltage (typ.) \\ V_{GE} = 15V / chip$ 



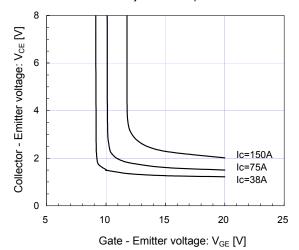
 $\label{eq:capacitance} \begin{tabular}{ll} [Inverter] \\ Capacitance vs. Collector-Emitter voltage (typ.) \\ $V_{GE}$=0V, f= 1MHz, Tj= 25°C \\ \end{tabular}$ 



 $\label{eq:continuous} \begin{tabular}{l} [Inverter] \\ Collector current vs. Collector-Emitter voltage (typ.) \\ Tj=150^{\circ}C\ /\ chip \\ \end{tabular}$ 



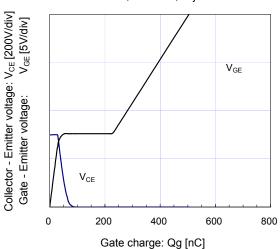
 $[Inverter] \\ Collector-Emitter voltage \ vs. \ Gate-Emitter voltage \ (typ.) \\ Tj= 25^{\circ}C \ / \ chip$ 



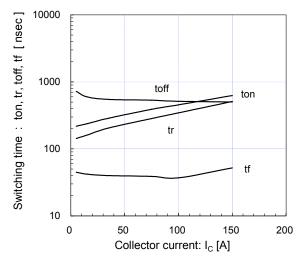
[ Inverter ]

Dynamic gate charge (typ.)

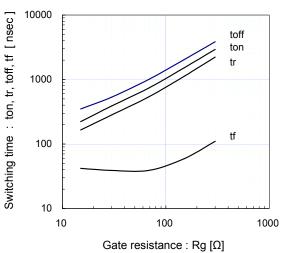
Vcc=300V, Ic=75A, Tj= 25°C



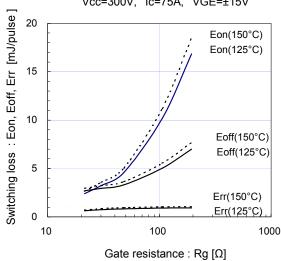
[Inverter]
Switching time vs. Collector current (typ.)
Vcc=300V, VGE=±15V, Rg=30Ω, Tj= 125°C



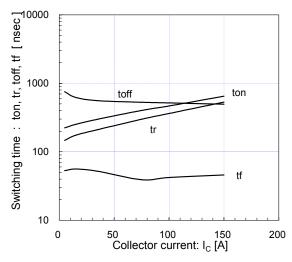
[Inverter]
Switching time vs. gate resistance (typ.)
Vcc=300V, Ic=75A, VGE=±15V, Tj= 125°C



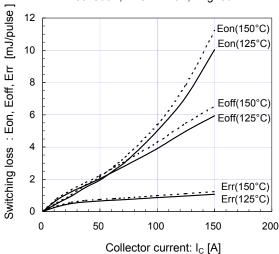
[ Inverter ]
Switching loss vs. gate resistance (typ.)
Vcc=300V, Ic=75A, VGE=±15V



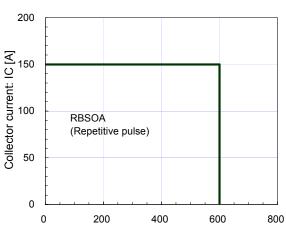
[ Inverter ] Switching time vs. Collector current (typ.) Vcc=300V, VGE= $\pm$ 15V, Rg=30 $\Omega$ , Tj= 150°C



 $\label{eq:continuous} \begin{tabular}{ll} [Inverter] \\ Switching loss vs. Collector current (typ.) \\ Vcc=300V, VGE=\pm15V, Rg=30\Omega \\ \end{tabular}$ 

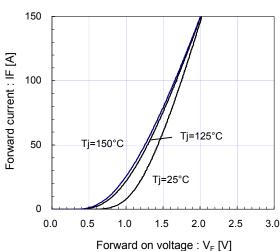


[ Inverter ] Reverse bias safe operating area (max.)  $+VGE=15V, -VGE <= 15V, RG >= 30\Omega \ , Tj <= 125^{\circ}C$ 

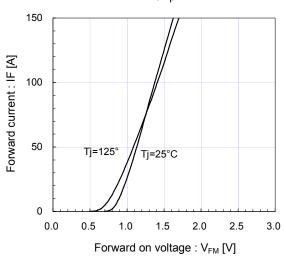


Collector-Emitter voltage : V<sub>CE</sub> [V]

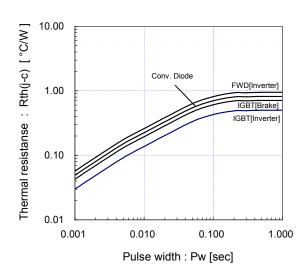
[ Inverter ]
Forward current vs. forward on voltage (typ.) chip



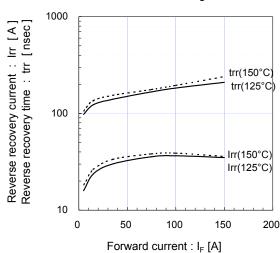
[ Converter ]
Forward current vs. forward on voltage (typ.) chip



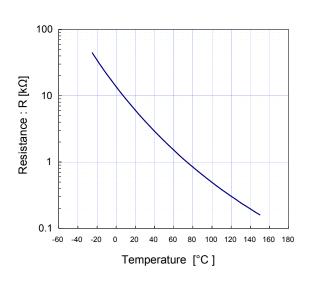
Transient thermal resistance (max.)



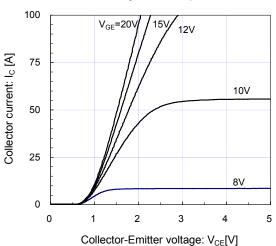
 $\label{eq:continuous} \begin{tabular}{ll} [Inverter] \\ Reverse recovery characteristics (typ.) \\ Vcc=300V, VGE=\pm15V, Rg=30\Omega \\ \end{tabular}$ 



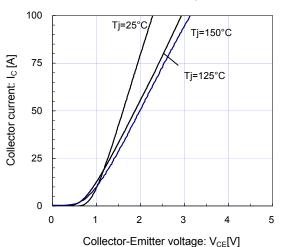
[ Thermistor ]
Temperature characteristic (typ.)



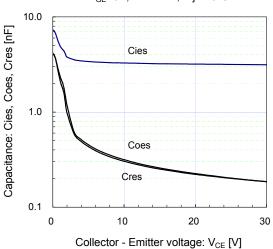
 $[\ Brake\ ]$  Collector current vs. Collector-Emitter voltage (typ.)  $Tj{=}\ 25^{\circ}C\ /\ chip$ 



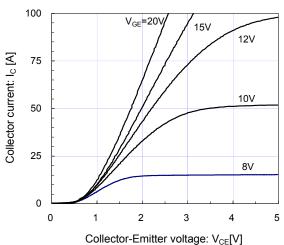
[ Brake ]
Collector current vs. Collector-Emitter voltage (typ.)
VGE=15V / chip



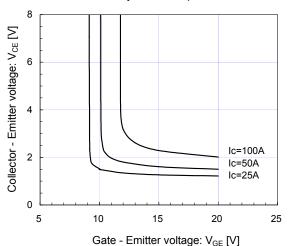
 $[\mbox{ Brake }] $$ Capacitance vs. Collector-Emitter voltage (typ.) $$ V_{GE}=0V, \ f= 1MHz, \ Tj= 25^{\circ}C $$$ 



 $[ \ Brake \ ]$  Collector current vs. Collector-Emitter voltage (typ.)  $Tj{=}\ 150^{\circ}C\ /\ chip$ 



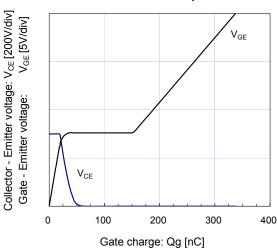
 $[\mbox{ Brake }] $$ Collector-Emitter voltage vs. Gate-Emitter voltage (typ.) $$ Tj= 25^{\circ}C\ /\ chip $$$ 



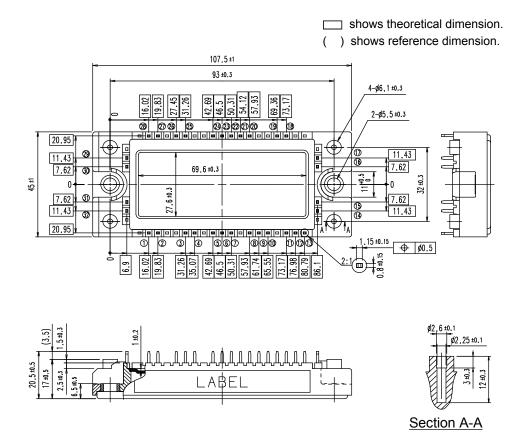
[ Brake ]

Dynamic gate charge (typ.)

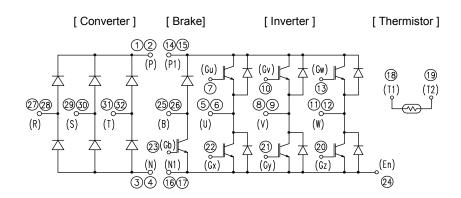
Vcc=300V, Ic=75A, Tj= 25°C



# ■ Outline Drawings, mm



## **■** Equivalent Circuit Schematic



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- Communications equipment (terminal devices)
- Measurement equipment

- Machine tools
- Audiovisual equipment
- Electrical home appliances
- Personal equipment
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