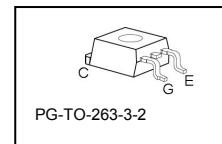
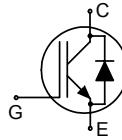


## Fast IGBT in NPT-technology with soft, fast recovery anti-parallel EmCon diode

- 75% lower  $E_{off}$  compared to previous generation combined with low conduction losses
- Short circuit withstand time – 10  $\mu\text{s}$
- Designed for frequency inverters for washing machines, fans, pumps and vacuum cleaners
- NPT-Technology for 600V applications offers:
  - very tight parameter distribution
  - high ruggedness, temperature stable behaviour
  - parallel switching capability
- Very soft, fast recovery anti-parallel EmCon diode
- Pb-free lead plating; RoHS compliant
- Qualified according to JEDEC<sup>1</sup> for target applications
- Complete product spectrum and PSpice Models : <http://www.infineon.com/igbt/>



Type	$V_{CE}$	$I_c$	$V_{CE(\text{sat})}$	$T_j$	Marking	Package
SKB06N60	600V	6A	2.3V	150°C	K06N60	PG-T0-263-3-2

### Maximum Ratings

Parameter	Symbol	Value	Unit
Collector-emitter voltage	$V_{CE}$	600	V
DC collector current	$I_c$		A
$T_C = 25^\circ\text{C}$		12	
$T_C = 100^\circ\text{C}$		6.9	
Pulsed collector current, $t_p$ limited by $T_{j\text{max}}$	$I_{C\text{puls}}$	24	
Turn off safe operating area $V_{CE} \leq 600\text{V}$ , $T_j \leq 150^\circ\text{C}$	-	24	
Diode forward current	$I_F$		
$T_C = 25^\circ\text{C}$		12	
$T_C = 100^\circ\text{C}$		6	
Diode pulsed current, $t_p$ limited by $T_{j\text{max}}$	$I_{F\text{puls}}$	24	
Gate-emitter voltage	$V_{GE}$	$\pm 20$	V
Short circuit withstand time <sup>2</sup>	$t_{sc}$	10	$\mu\text{s}$
$V_{GE} = 15\text{V}$ , $V_{CC} \leq 600\text{V}$ , $T_j \leq 150^\circ\text{C}$			
Power dissipation	$P_{\text{tot}}$	68	W
$T_C = 25^\circ\text{C}$			
Operating junction and storage temperature	$T_j$ , $T_{stg}$	-55...+150	$^\circ\text{C}$
Soldering temperature (reflow soldering, MSL1)	$T_s$	245	$^\circ\text{C}$

<sup>1</sup> J-STD-020 and JESD-022

<sup>2</sup> Allowed number of short circuits: <1000; time between short circuits: >1s.

**Thermal Resistance**

Parameter	Symbol	Conditions	Max. Value	Unit
<b>Characteristic</b>				
IGBT thermal resistance, junction – case	$R_{thJC}$		1.85	K/W
Diode thermal resistance, junction – case	$R_{thJCD}$		3.5	
SMD version, device on PCB <sup>1)</sup>	$R_{thJA}$		40	

**Electrical Characteristic, at  $T_j = 25^\circ\text{C}$ , unless otherwise specified**

Parameter	Symbol	Conditions	Value			Unit
			min.	Typ.	max.	
<b>Static Characteristic</b>						
Collector-emitter breakdown voltage	$V_{(BR)CES}$	$V_{GE}=0\text{V}, I_C=500\mu\text{A}$	600	-	-	V
Collector-emitter saturation voltage	$V_{CE(\text{sat})}$	$V_{GE} = 15\text{V}, I_C=6\text{A}$ $T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$	1.7 -	2.0 2.3	2.4 2.8	
Diode forward voltage	$V_F$	$V_{GE}=0\text{V}, I_F=6\text{A}$ $T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$	1.2 -	1.4 1.25	1.8 1.65	
Gate-emitter threshold voltage	$V_{GE(\text{th})}$	$I_C=250\mu\text{A}, V_{CE}=V_{GE}$	3	4	5	
Zero gate voltage collector current	$I_{CES}$	$V_{CE}=600\text{V}, V_{GE}=0\text{V}$ $T_j=25^\circ\text{C}$ $T_j=150^\circ\text{C}$	- -	-	20 700	$\mu\text{A}$
Gate-emitter leakage current	$I_{GES}$	$V_{CE}=0\text{V}, V_{GE}=20\text{V}$	-	-	100	nA
Transconductance	$g_{fs}$	$V_{CE}=20\text{V}, I_C=6\text{A}$	-	4.2	-	S

**Dynamic Characteristic**

Input capacitance	$C_{iss}$	$V_{CE}=25\text{V},$	-	350	420	pF
Output capacitance	$C_{oss}$	$V_{GE}=0\text{V},$	-	38	46	
Reverse transfer capacitance	$C_{rss}$	$f=1\text{MHz}$	-	23	28	
Gate charge	$Q_{\text{Gate}}$	$V_{CC}=480\text{V}, I_C=6\text{A}$ $V_{GE}=15\text{V}$	-	32	42	nC
Internal emitter inductance measured 5mm (0.197 in.) from case	$L_E$		-	7	-	nH
Short circuit collector current <sup>2)</sup>	$I_{C(\text{SC})}$	$V_{GE}=15\text{V}, t_{SC}\leq 10\mu\text{s}$ $V_{CC} \leq 600\text{V},$ $T_j \leq 150^\circ\text{C}$	-	60	-	A

<sup>1)</sup> Device on 50mm\*50mm\*1.5mm epoxy PCB FR4 with 6cm<sup>2</sup> (one layer, 70μm thick) copper area for collector connection. PCB is vertical without blown air.

<sup>2)</sup> Allowed number of short circuits: <1000; time between short circuits: >1s.

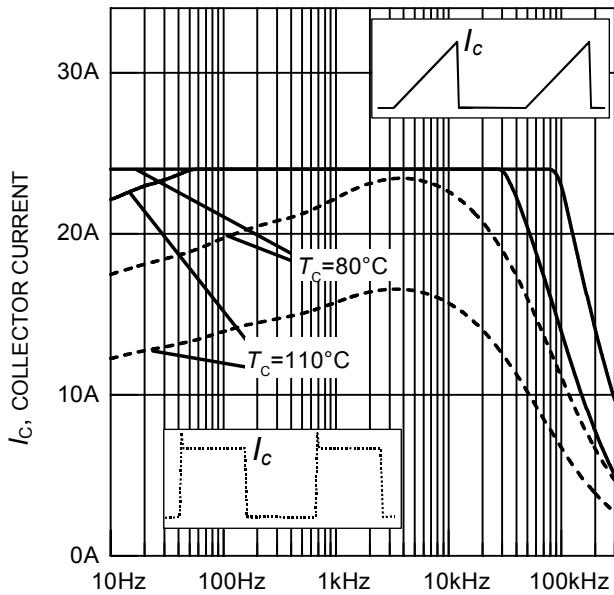
Switching Characteristic, Inductive Load, at  $T_j=25^\circ\text{C}$ 

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
<b>IGBT Characteristic</b>						
Turn-on delay time	$t_{d(on)}$	$T_j=25^\circ\text{C}$ , $V_{CC}=400\text{V}$ , $I_C=6\text{A}$ , $V_{GE}=0/15\text{V}$ , $R_G=50\Omega$ , $L_\sigma^{(1)}=180\text{nH}$ , $C_\sigma^{(1)}=250\text{pF}$ Energy losses include “tail” and diode reverse recovery.	-	25	30	ns
Rise time	$t_r$		-	18	22	
Turn-off delay time	$t_{d(off)}$		-	220	264	
Fall time	$t_f$		-	54	65	
Turn-on energy	$E_{on}$		-	0.110	0.127	mJ
Turn-off energy	$E_{off}$		-	0.105	0.137	
Total switching energy	$E_{ts}$		-	0.215	0.263	
<b>Anti-Parallel Diode Characteristic</b>						
Diode reverse recovery time	$t_{rr}$	$T_j=25^\circ\text{C}$ , $V_R=200\text{V}$ , $I_F=6\text{A}$ , $di_F/dt=200\text{A}/\mu\text{s}$	-	200	-	ns
	$t_s$		-	17	-	
	$t_F$		-	183	-	
Diode reverse recovery charge	$Q_{rr}$		-	200	-	nC
Diode peak reverse recovery current	$I_{rrm}$		-	2.8	-	A
Diode peak rate of fall of reverse recovery current during $t_b$	$di_{rr}/dt$		-	180	-	A/ $\mu\text{s}$

Switching Characteristic, Inductive Load, at  $T_j=150^\circ\text{C}$ 

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
<b>IGBT Characteristic</b>						
Turn-on delay time	$t_{d(on)}$	$T_j=150^\circ\text{C}$ , $V_{CC}=400\text{V}$ , $I_C=6\text{A}$ , $V_{GE}=0/15\text{V}$ , $R_G=50\Omega$ , $L_\sigma^{(1)}=180\text{nH}$ , $C_\sigma^{(1)}=250\text{pF}$ Energy losses include “tail” and diode reverse recovery.	-	24	29	ns
Rise time	$t_r$		-	17	20	
Turn-off delay time	$t_{d(off)}$		-	248	298	
Fall time	$t_f$		-	70	84	
Turn-on energy	$E_{on}$		-	0.167	0.192	mJ
Turn-off energy	$E_{off}$		-	0.153	0.199	
Total switching energy	$E_{ts}$		-	0.320	0.391	
<b>Anti-Parallel Diode Characteristic</b>						
Diode reverse recovery time	$t_{rr}$	$T_j=150^\circ\text{C}$ , $V_R=200\text{V}$ , $I_F=6\text{A}$ , $di_F/dt=200\text{A}/\mu\text{s}$	-	290	-	ns
	$t_s$		-	27	-	
	$t_F$		-	263	-	
Diode reverse recovery charge	$Q_{rr}$		-	500	-	nC
Diode peak reverse recovery current	$I_{rrm}$		-	5.0	-	A
Diode peak rate of fall of reverse recovery current during $t_b$	$di_{rr}/dt$		-	200	-	A/ $\mu\text{s}$

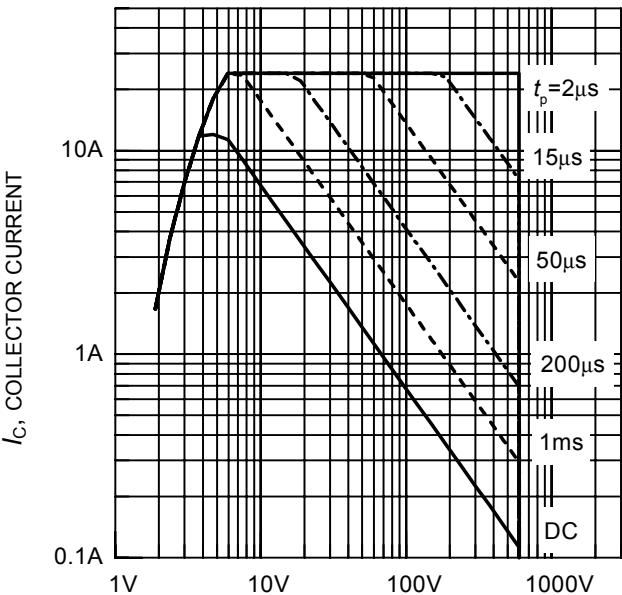
<sup>1)</sup> Leakage inductance  $L_\sigma$  and Stray capacity  $C_\sigma$  due to dynamic test circuit in Figure E.



$f$ , SWITCHING FREQUENCY

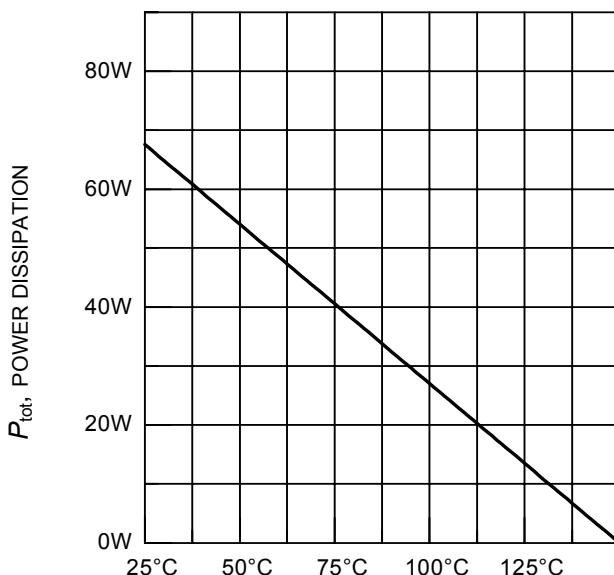
**Figure 1. Collector current as a function of switching frequency**

( $T_j \leq 150^\circ\text{C}$ ,  $D = 0.5$ ,  $V_{\text{CE}} = 400\text{V}$ ,  
 $V_{\text{GE}} = 0/+15\text{V}$ ,  $R_G = 50\Omega$ )



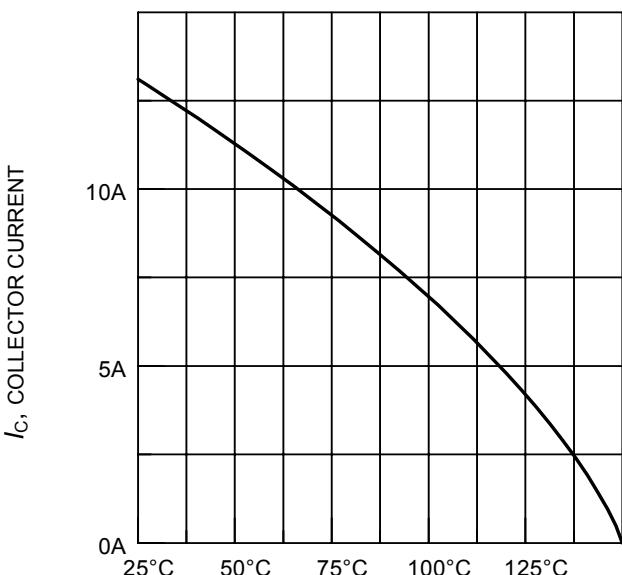
$V_{\text{CE}}$ , COLLECTOR-EMITTER VOLTAGE

**Figure 2. Safe operating area**  
 $(D = 0, T_C = 25^\circ\text{C}, T_j \leq 150^\circ\text{C})$



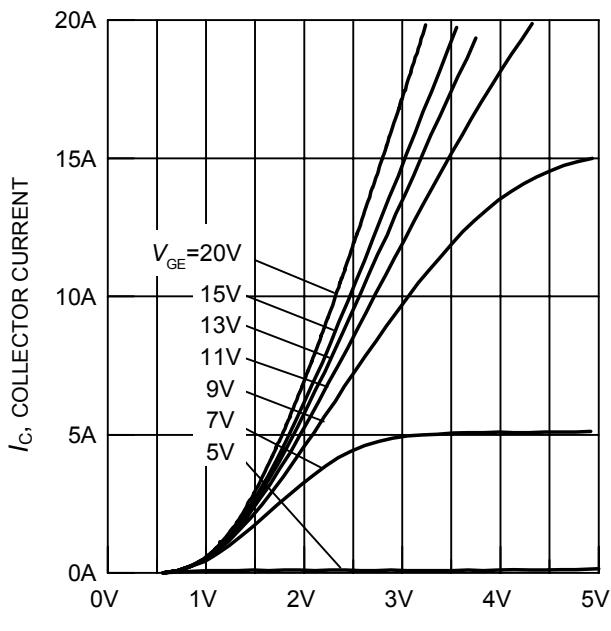
$T_C$ , CASE TEMPERATURE

**Figure 3. Power dissipation as a function of case temperature**  
 $(T_j \leq 150^\circ\text{C})$

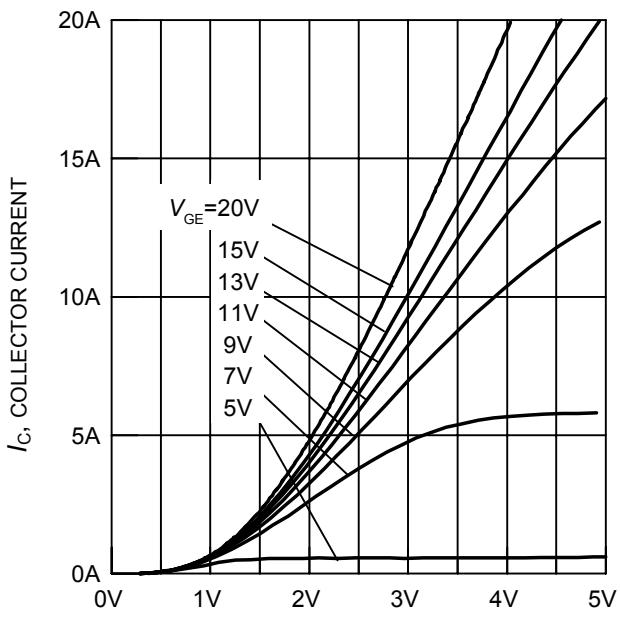


$T_C$ , CASE TEMPERATURE

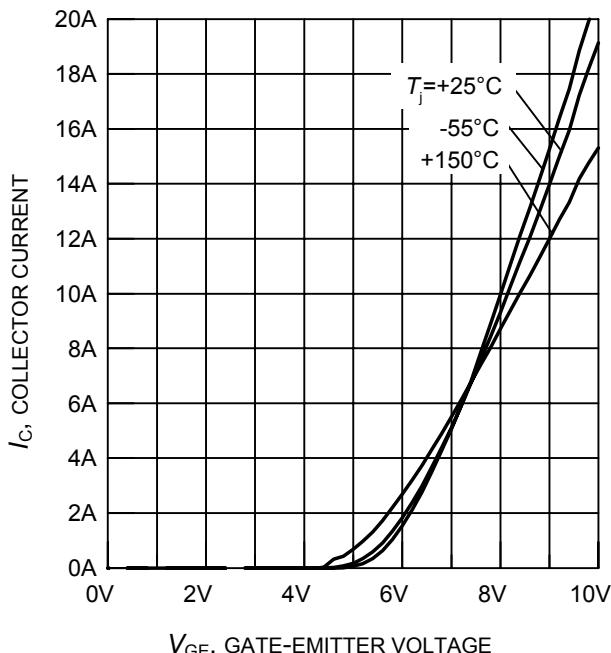
**Figure 4. Collector current as a function of case temperature**  
 $(V_{\text{GE}} \leq 15\text{V}, T_j \leq 150^\circ\text{C})$


 $V_{CE}$ , COLLECTOR-EMITTER VOLTAGE

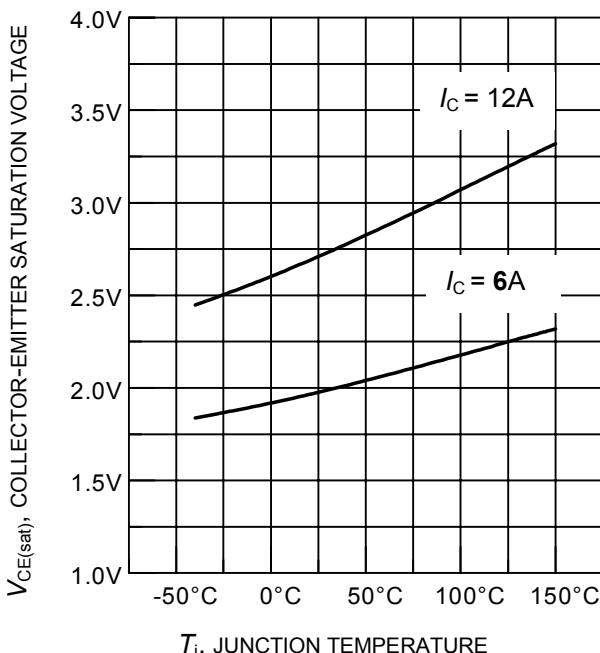
**Figure 5. Typical output characteristics**  
( $T_j = 25^\circ\text{C}$ )


 $V_{CE}$ , COLLECTOR-EMITTER VOLTAGE

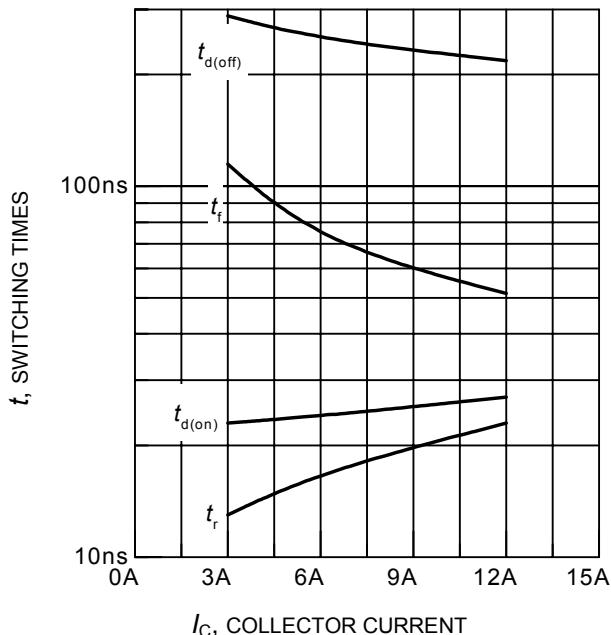
**Figure 6. Typical output characteristics**  
( $T_j = 150^\circ\text{C}$ )


 $V_{GE}$ , GATE-EMITTER VOLTAGE

**Figure 7. Typical transfer characteristics**  
( $V_{CE} = 10\text{V}$ )

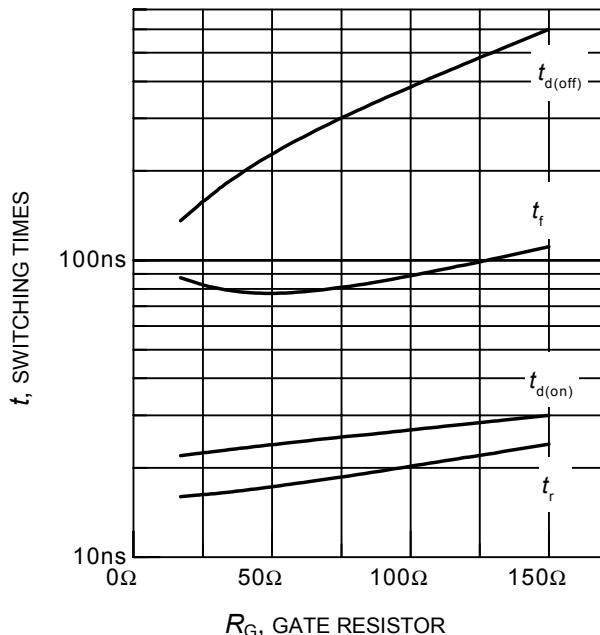

 $T_j$ , JUNCTION TEMPERATURE

**Figure 8. Typical collector-emitter saturation voltage as a function of junction temperature**  
( $V_{GE} = 15\text{V}$ )


 $I_C$ , COLLECTOR CURRENT

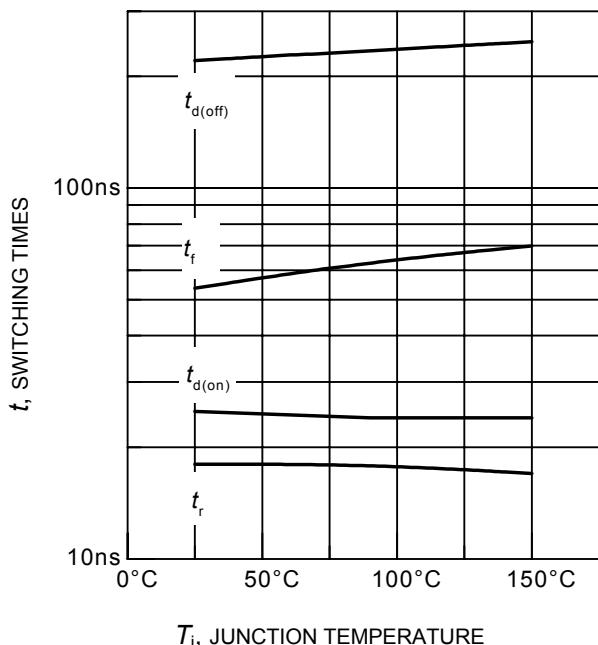
**Figure 9. Typical switching times as a function of collector current**

(inductive load,  $T_j = 150^\circ\text{C}$ ,  $V_{CE} = 400\text{V}$ ,  $V_{GE} = 0/+15\text{V}$ ,  $R_G = 50\Omega$ , Dynamic test circuit in Figure E)


 $R_G$ , GATE RESISTOR

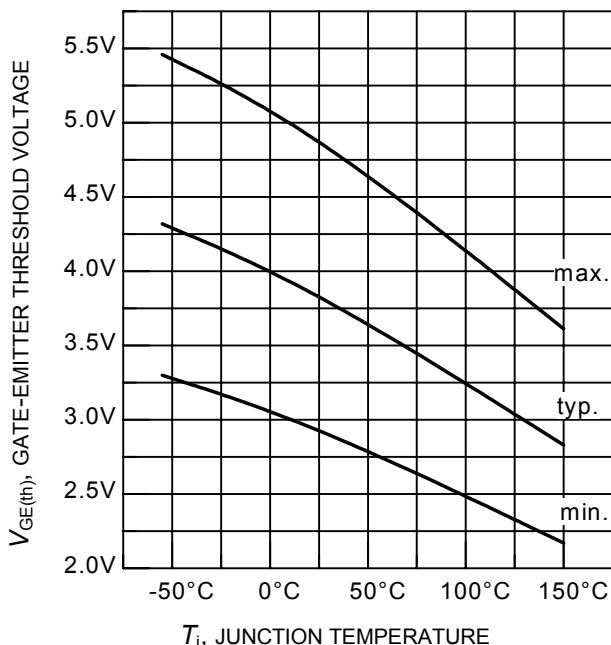
**Figure 10. Typical switching times as a function of gate resistor**

(inductive load,  $T_j = 150^\circ\text{C}$ ,  $V_{CE} = 400\text{V}$ ,  $V_{GE} = 0/+15\text{V}$ ,  $I_C = 6\text{A}$ , Dynamic test circuit in Figure E)


 $T_j$ , JUNCTION TEMPERATURE

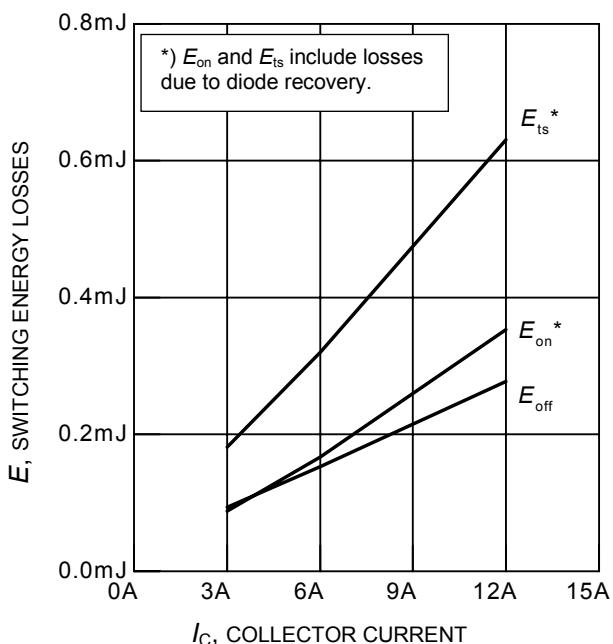
**Figure 11. Typical switching times as a function of junction temperature**

(inductive load,  $V_{CE} = 400\text{V}$ ,  $V_{GE} = 0/+15\text{V}$ ,  $I_C = 6\text{A}$ ,  $R_G = 50\Omega$ , Dynamic test circuit in Figure E)


 $T_j$ , JUNCTION TEMPERATURE

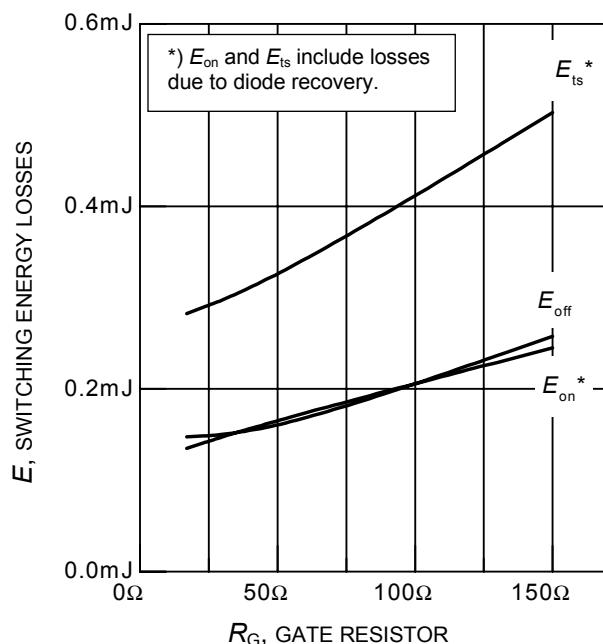
**Figure 12. Gate-emitter threshold voltage as a function of junction temperature**

( $I_C = 0.25\text{mA}$ )



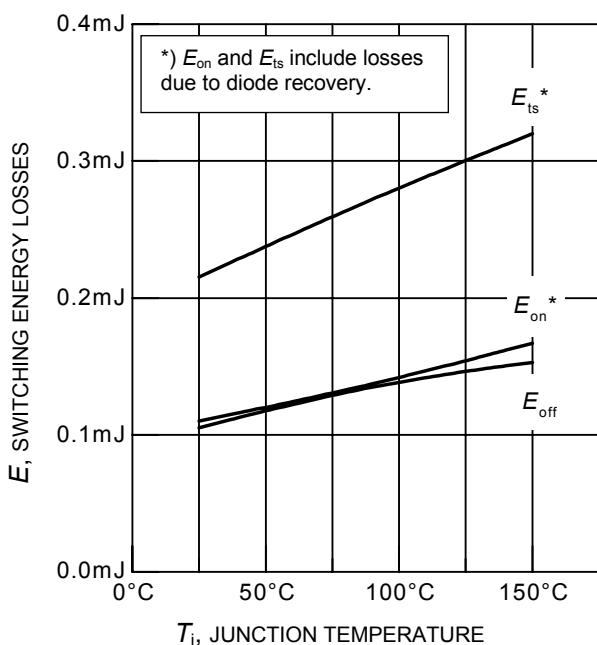
**Figure 13. Typical switching energy losses as a function of collector current**

(inductive load,  $T_j = 150^\circ\text{C}$ ,  $V_{\text{CE}} = 400\text{V}$ ,  
 $V_{\text{GE}} = 0/+15\text{V}$ ,  $R_G = 50\Omega$ ,  
Dynamic test circuit in Figure E)



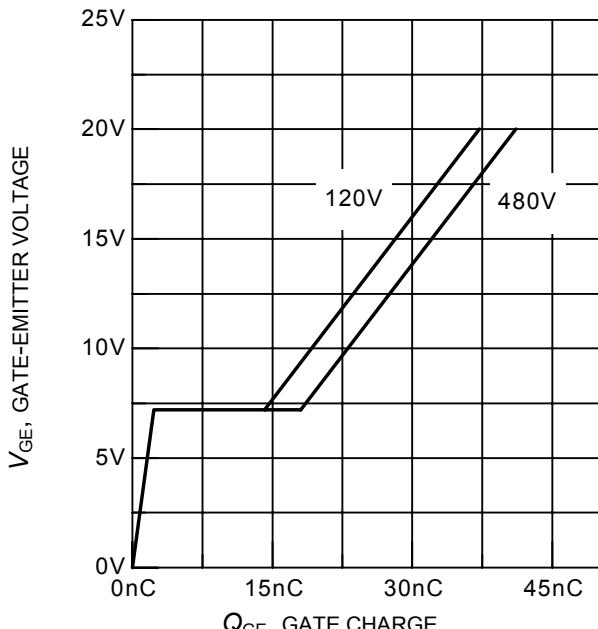
**Figure 14. Typical switching energy losses as a function of gate resistor**

(inductive load,  $T_j = 150^\circ\text{C}$ ,  $V_{\text{CE}} = 400\text{V}$ ,  
 $V_{\text{GE}} = 0/+15\text{V}$ ,  $I_C = 6\text{A}$ ,  
Dynamic test circuit in Figure E)

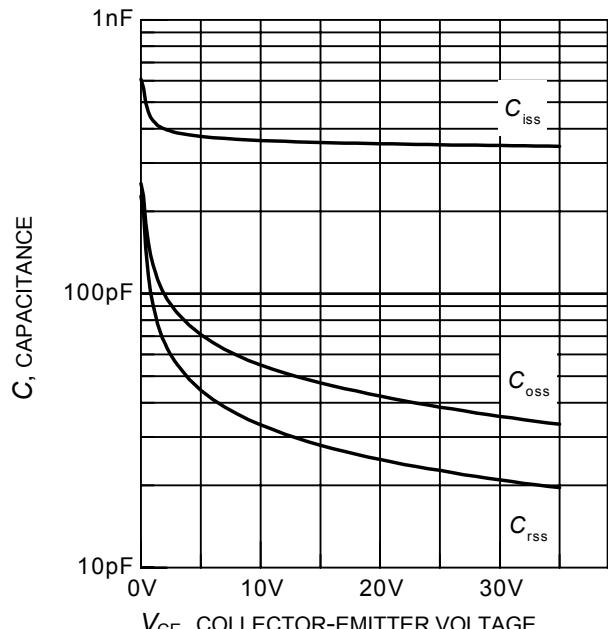


**Figure 15. Typical switching energy losses as a function of junction temperature**

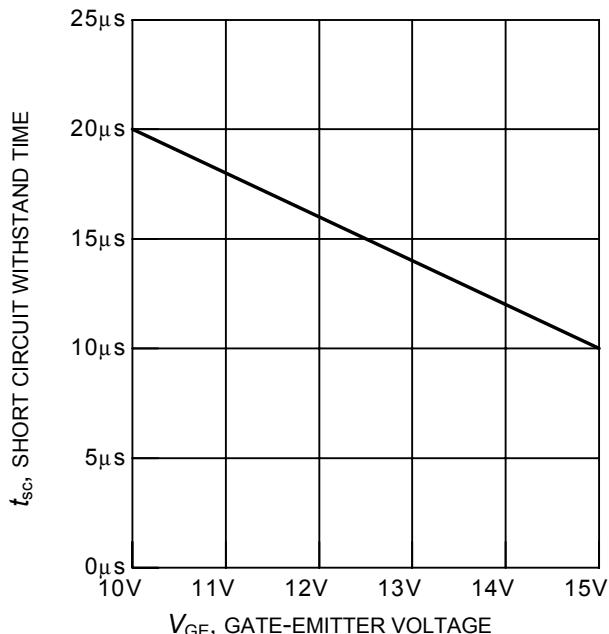
(inductive load,  $V_{\text{CE}} = 400\text{V}$ ,  $V_{\text{GE}} = 0/+15\text{V}$ ,  
 $I_C = 6\text{A}$ ,  $R_G = 50\Omega$ ,  
Dynamic test circuit in Figure E)



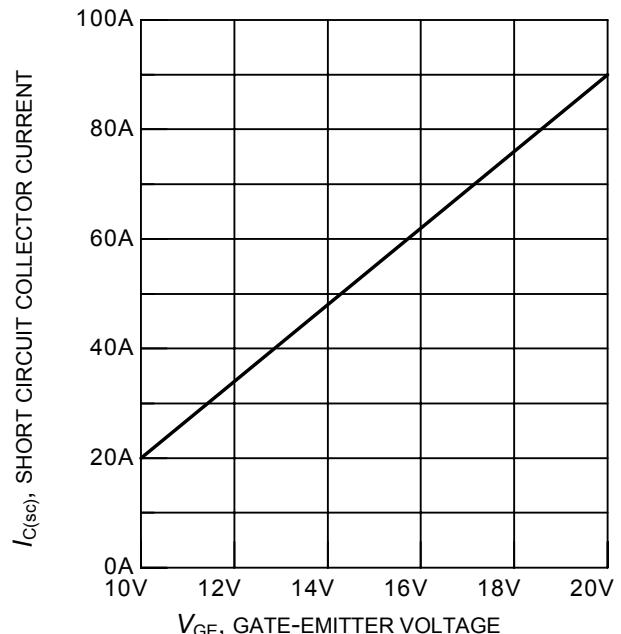
**Figure 16. Typical gate charge**  
( $I_C = 6\text{A}$ )



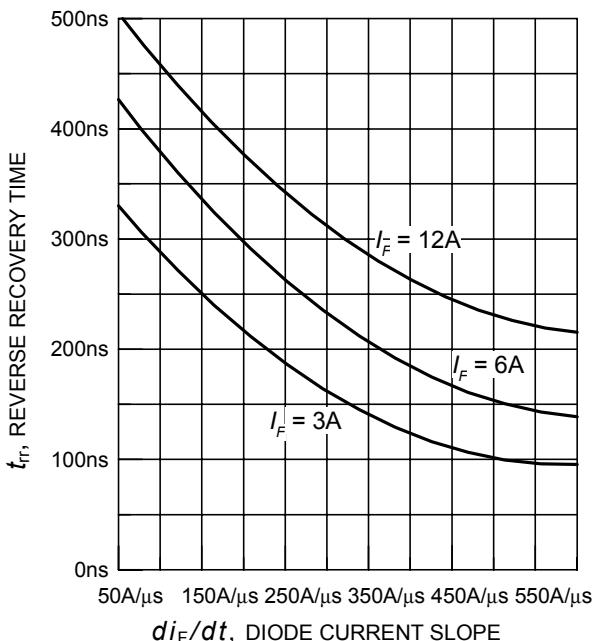
**Figure 17. Typical capacitance as a function of collector-emitter voltage**  
( $V_{GE} = 0\text{V}$ ,  $f = 1\text{MHz}$ )



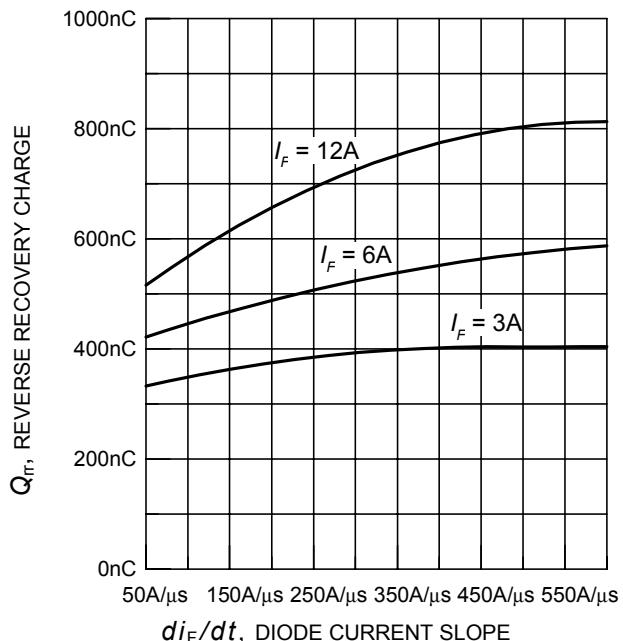
**Figure 18. Short circuit withstand time as a function of gate-emitter voltage**  
( $V_{CE} = 600\text{V}$ , start at  $T_j = 25^\circ\text{C}$ )



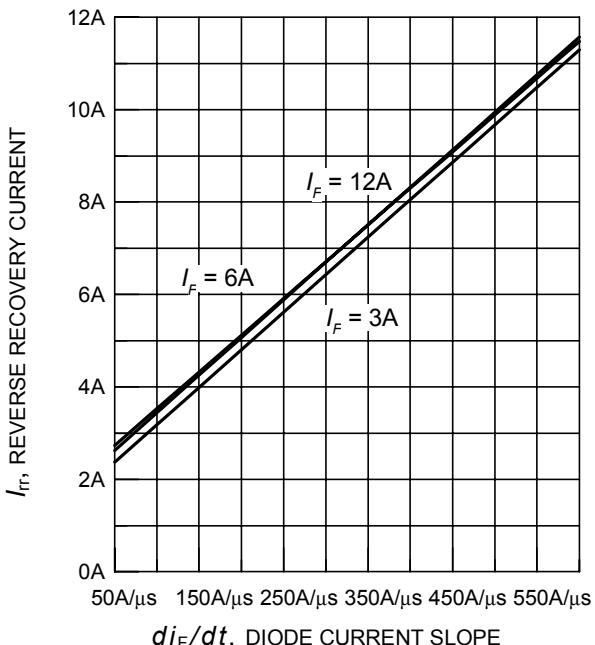
**Figure 19. Typical short circuit collector current as a function of gate-emitter voltage**  
( $V_{CE} \leq 600\text{V}$ ,  $T_j = 150^\circ\text{C}$ )



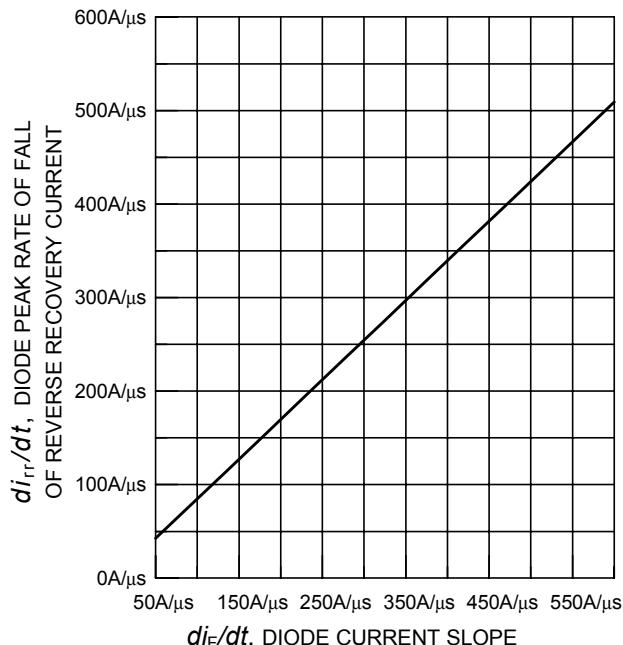
**Figure 20. Typical reverse recovery time as a function of diode current slope**  
 $(V_R = 200V, T_j = 125^{\circ}C,$   
 Dynamic test circuit in Figure E)



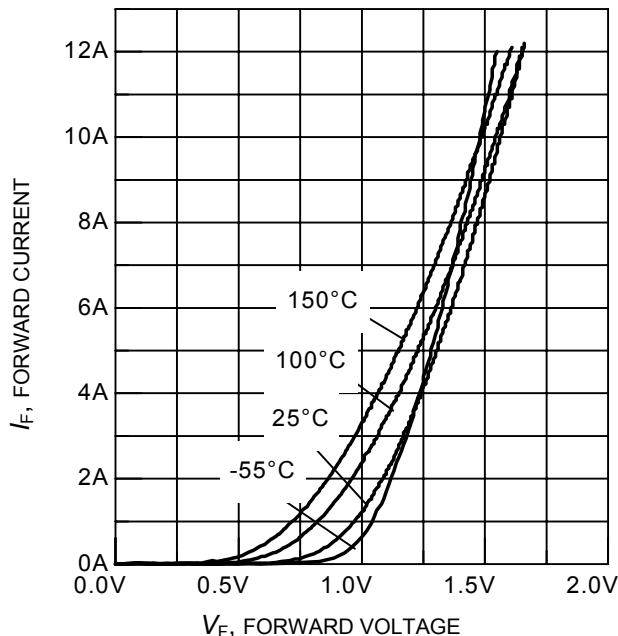
**Figure 21. Typical reverse recovery charge as a function of diode current slope**  
 $(V_R = 200V, T_j = 125^{\circ}C,$   
 Dynamic test circuit in Figure E)



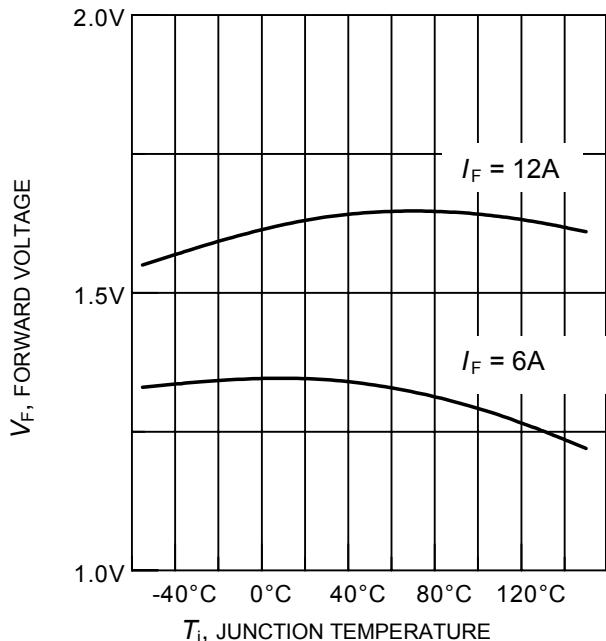
**Figure 22. Typical reverse recovery current as a function of diode current slope**  
 $(V_R = 200V, T_j = 125^{\circ}C,$   
 Dynamic test circuit in Figure E)



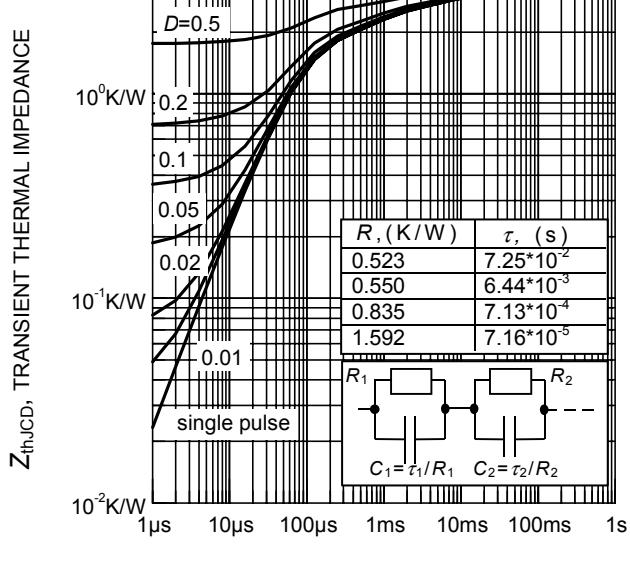
**Figure 23. Typical diode peak rate of fall of reverse recovery current as a function of diode current slope**  
 $(V_R = 200V, T_j = 125^{\circ}C,$   
 Dynamic test circuit in Figure E)



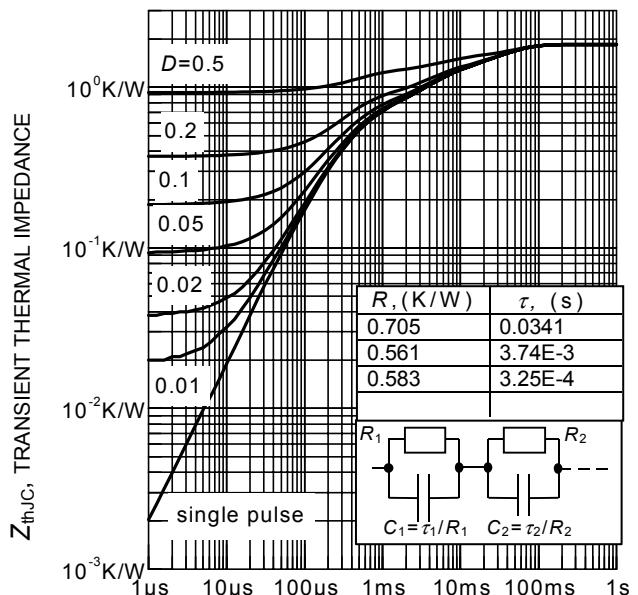
**Figure 24. Typical diode forward current as a function of forward voltage**



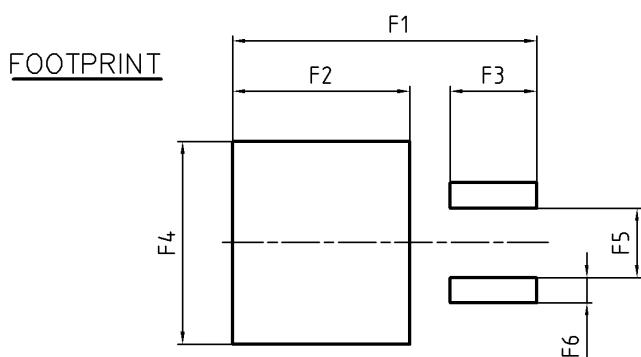
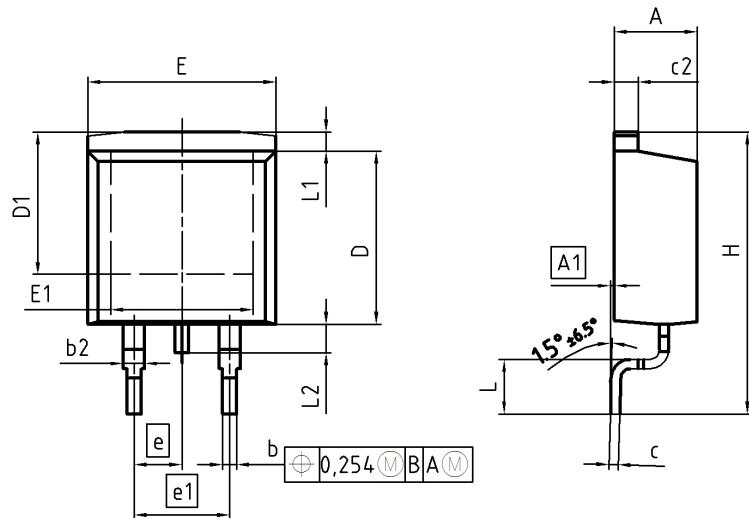
**Figure 25. Typical diode forward voltage as a function of junction temperature**



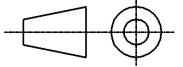
**Figure 26. Diode transient thermal impedance as a function of pulse width**  
 $(D = t_p / T)$

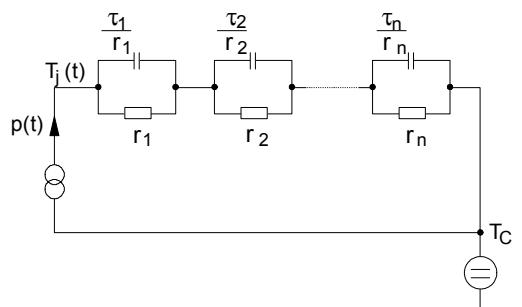
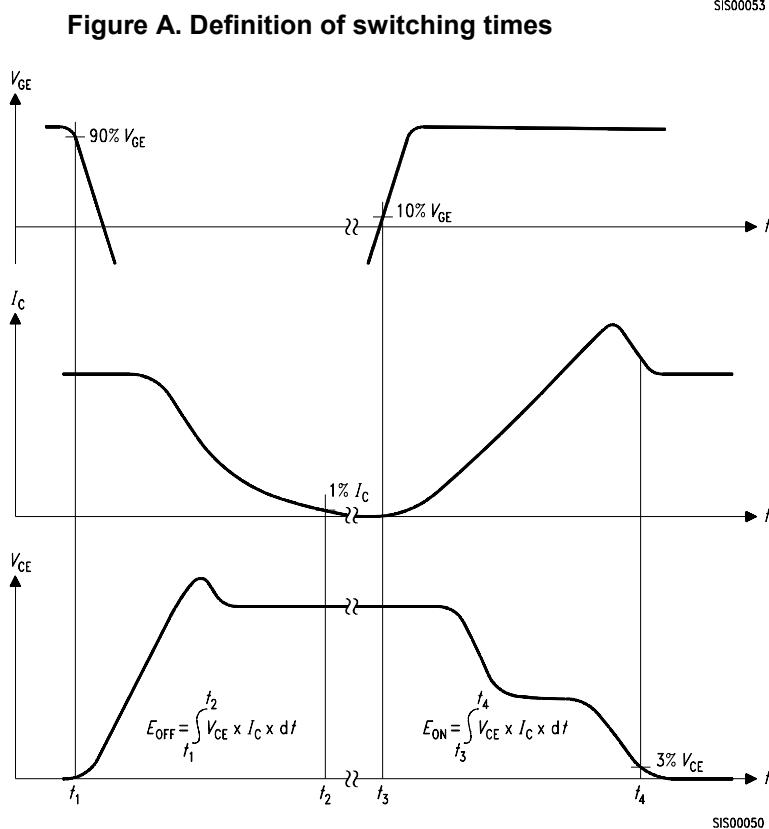
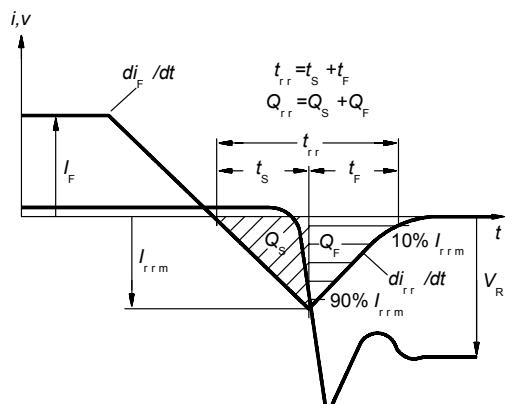
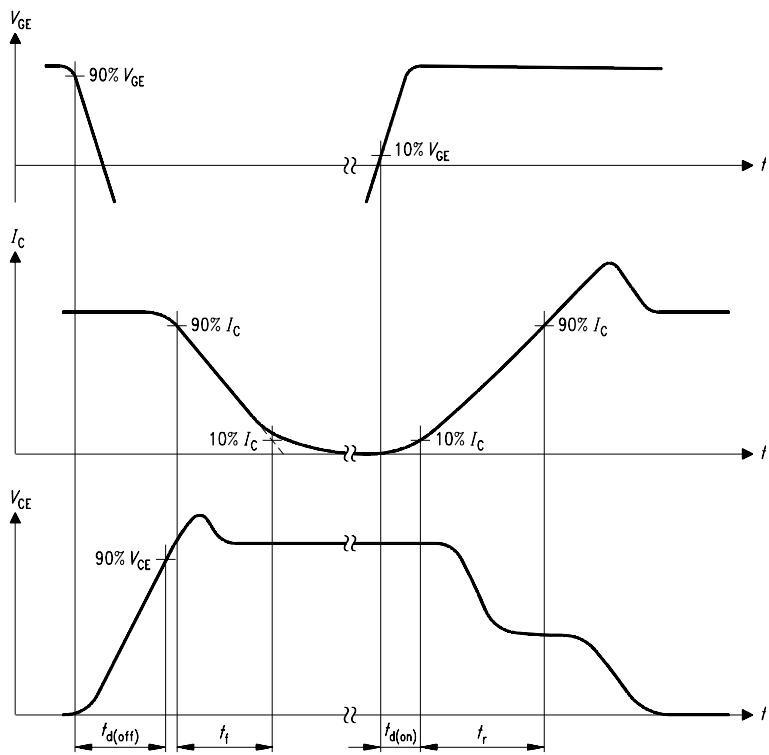


**Figure 27. IGBT transient thermal impedance as a function of pulse width**  
 $(D = t_p / T)$

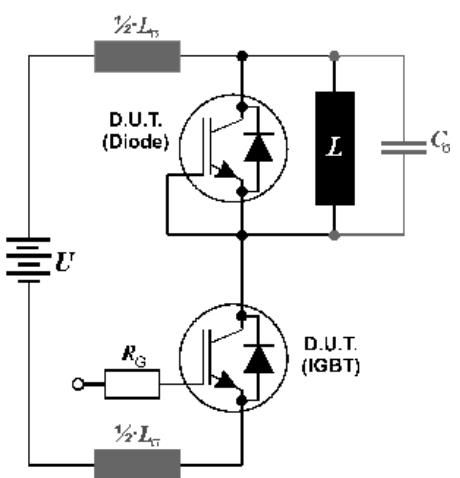
**PG-T0263-3-2**


DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	4.30	4.57	0.169	0.180
A1	0.00	0.25	0.000	0.010
b	0.65	0.85	0.026	0.033
b2	0.95	1.15	0.037	0.045
c	0.33	0.65	0.013	0.026
c2	1.17	1.40	0.046	0.055
D	8.51	9.45	0.335	0.372
D1	7.10	7.90	0.280	0.311
E	9.80	10.31	0.386	0.406
E1	6.50	8.60	0.256	0.339
e	2.54		0.100	
e1	5.08		0.200	
N	2		2	
H	14.61	15.88	0.575	0.625
L	2.29	3.00	0.090	0.118
L1	0.70	1.60	0.028	0.063
L2	1.00	1.78	0.039	0.070
F1	16.05	16.25	0.632	0.640
F2	9.30	9.50	0.366	0.374
F3	4.50	4.70	0.177	0.185
F4	10.70	10.90	0.421	0.429
F5	3.65	3.85	0.144	0.152
F6	1.25	1.45	0.049	0.057

DOCUMENT NO. Z8B00003324
SCALE      0 0      5      5 7.5mm
EUROPEAN PROJECTION

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**Figure D. Thermal equivalent circuit**



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