

6MBI550V-120-50

IGBT Modules

IGBT MODULE (V series) 1200V / 550A / 6 in one package

■ Features

Compact Package P.C.Board Mount Low VcE (sat) **RoHS** Compliant product

■ Applications

Inverter for Motor Drive AC and DC Servo Drive Amplifier Uninterruptible Power Supply Industrial machines, such as welding machines



■ Maximum Ratings and Characteristics

● Absolute Maximum Ratings (at T_c=25°C unless otherwise specified)

Items		Symbols	Conditions	Conditions		Units		
	Collector-Emitter voltage		V _{CES}				V	
	Gate-Emitter voltage		V _{GES}			±20	V	
	Collector current		Ic	Continuous	Tc=25°C	750		
rter				Continuous	Tc=100°C	550		
nve			I _{C pulse}	1ms	Ims		Α	
=			-Ic		1ms			
			-Ic pulse	1ms				
	Collector power dissipation		Pc	1 device	1 device		W	
Junction temperature		T;						
Operating junciton temperature (under switching conditions)		Тјор			150	°C		
Case temperature		Tc			125			
Sto	Storage temperature		T _{stg}			-40 ~ +125		
	olation voltage	Between terminal and copper base (*1)	.,	10 1		2500	VAC	
ISO		Between thermistor and others (*2)	V _{iso}	AC : Tmin.	AC : 1min.			
0-	crew torque	Mounting (*3)				3.5	NI	
SC!		Terminals (*4)	1-			4.5	N m	

Note *1: All terminals should be connected together during the test.

Note *2: Two thermistor terminals should be connected together, other terminals should be connected together and shorted to base plate during the test. Note *3: Recommendable Value : 2.5-3.5 Nm (M5)

Note *4: Recommendable Value: 3.5-4.5 Nm (M6)

● Electrical characteristics (at T_j= 25°C unless otherwise specified)

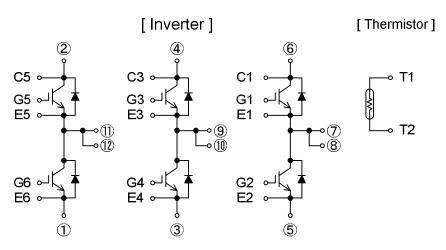
Items		Cumbala	Conditions		Characteristics			Heite
ite	ms	Symbols	Conditions		min.	typ.	max.	Units
	Zero gate voltage collector current	Ices	V _{GE} = 0V, V _{CE} = 1200V		-	-	3.0	mA
Inverter	Gate-Emitter leakage current	Iges	I _{GES} V _{CE} = 0V, V _{GE} = ±20V		-	-	600	nA
	Gate-Emitter threshold voltage	V _{GE (th)}	V _{CE} = 20V, I _C = 600mA		6.0	6.5	7.0	V
	Collector-Emitter saturation voltage	.,	V _{GE} = 15V I _C = 600A	T _j =25°C	-	2.50	2.95	V
		V _{CE (sat)} (terminal)		T _j =125°C	-	2.85	-	
		(terrillial)		T _j =150°C	-	2.90	-	
			V _{GE} = 15V I _C = 600A	T _j =25°C	-	1.85	2.10	
		V _{CE (sat)} (chip)		T _j =125°C	-	2.20	-	
		(Criip)		T _j =150°C	-	2.25	-	
	Internal gate resistance	R _G (int)	-		-	1.10	-	Ω
	Input capacitance	Cies	V _{CE} = 10V, V _{GE} = 0V, f = 1MHz		-	48	-	nF
	Input capacitance Turn-on time	t on	V 000V	-	550	-	nsec	
		tr	V _{cc} = 600V I _c = 600A	-	180	-		
		t r (i)	V _{GE} = ±15V		-	120		-
		toff	$R_G = 0.62\Omega$	-	1050	-		
	Turn-off time	tr	Ls = 80nH		-	110		-
	Forward on voltage		V _{GE} = 0V, I _F = 600A	T _j =25°C	-	2.40	2.85	.,
		V _F (terminal)		T _j =125°C	-	2.55	-	
		(terrilinal)		T _j =150°C	-	2.50	-	
				T _i =25°C - 1	1.75	2.20	V	
		V _F	V _{GE} = 0V, I _F = 600A	T _j =125°C	-	1.90	-	
		(chip)		T _j =150°C	-	1.85	-	
	leverse recovery time t_{rr} $I_F = 600A$			-	200	-	nsec	
Į.	Basistana a	Б	T = 25°C		-	5000	-	Ω
mist	Resistance B value	R	T = 100°C		465	495	520	
The	B value	В	T = 25 / 50°C		3305	3375	3450	K

● Thermal resistance characteristics

Items	Symbols	Conditions	Characteristics			Units
items		Conditions	min.	typ.	max.	Ullits
Thermal resistance (1device)	В	Inverter IGBT	-	-	0.060	°C/W
	R _{th(j-c)}	Inverter FWD	-	-	0.100	
Contact thermal resistance (1device) (*1)	R _{th(c-f)}	with Thermal Compound	-	0.0167	-	

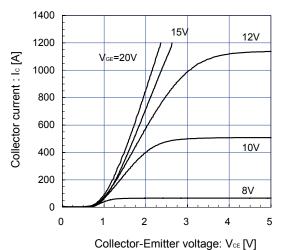
Note *1: This is the value which is defined mounting on the additional cooling fin with thermal compound.

■ Equivalent Circuit Schematic

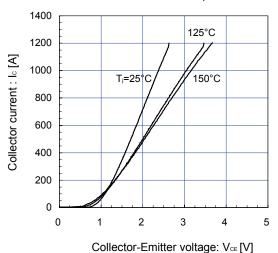


■ Characteristics (Representative)

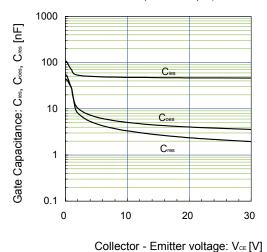
 $[Inverter\] \\ Collector\ current\ vs.\ Collector-Emitter\ voltage\ (typ.) \\ T_i=25^{\circ}C\ /\ chip$



[Inverter] Collector current vs. Collector-Emitter voltage (typ.) $V_{\text{GE}} = 15 \text{V / chip}$

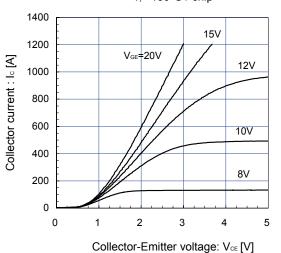


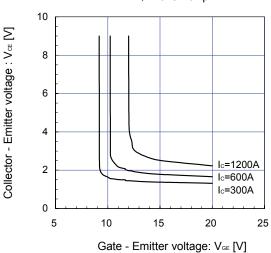
 $\label{eq:continuous} \begin{tabular}{ll} [Inverter] \\ Gate Capacitance vs. Collector-Emitter voltage (typ.) \\ V_{\text{GE}}=0V, \ f= 1MHz, \ T_{j}= 25^{\circ}C \end{tabular}$



[Inverter]

Collector current vs. Collector-Emitter voltage (typ.) $T_i = 150^{\circ}C / chip$

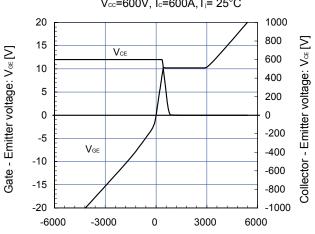




[Inverter]

Dynamic gate charge (typ.)

Vcc=600V, Ic=600A,Tj= 25°C



Gate charge: Q_G [nC]

[Inverter]

Switching time vs. Collector current (typ.)

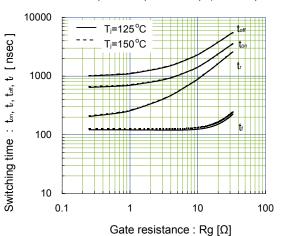
Switching time vs. Collector current (typ.) V_{CC} =600V, V_{GE} =±15V, Rg=0.62 Ω , T_{j} = 25°C 10000 1000 tor

[Inverter]

Switching time : ton, tr, toff, tr [nsec] 100 tr 10 500 1000 1500 0 Collector current: Ic [A]

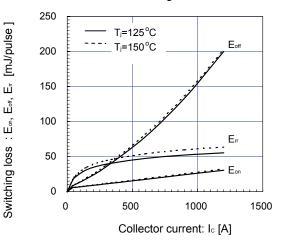
 V_{CC} =600V, V_{GE} =±15V, Rg=0.62 Ω , T_{j} = 125°C, 150°C 10000 T_j=125°C Switching time : ton, t, toff, tr [nsec] T_j=150°C 1000 100 10 1000 1500 500 0

[Inverter] Switching time vs. gate resistance (typ.) V_{cc} =600V, I_c =550A, V_{GE} =±15V, T_j = 125°C, 150°C

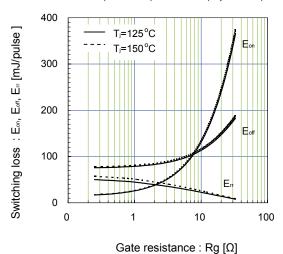


[Inverter] Switching loss vs. Collector current (typ.) V_{CC} =600V, V_{GE} =±15V, Rg=0.62 Ω , T_i=125°C, 150°C

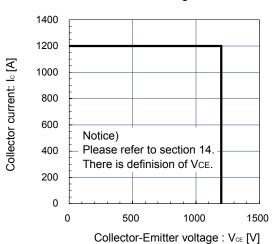
Collector current: Ic [A]



[Inverter] Switching loss vs. gate resistance (typ.) $V_{\text{CC}} \! = \! 600 V, \; I_{\text{C}} \! = \! 600 A, \; V_{\text{GE}} \! = \! \pm 15 V, \; T_{j} \! = \! 125^{\circ} C, \; 150^{\circ} C$



[Inverter] Reverse bias safe operating area (max.) $+V_{GE}$ =15V, $-V_{GE}$ ≤= 15V, Rg ≥= 0.62Ω, T_{j} = 150°C



Forward current: IF [A]

200

0

[INVERTER]
Forward Current vs. Forward Voltage (typ.)
chip

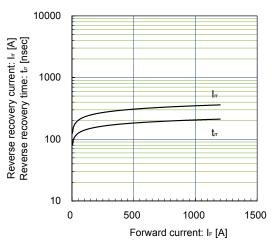
1400
1200
T_j=25°C

1000
800
400
150°C

,)

3

 $[INVERTER] $$ Reverse Recovery Characteristics (typ.) $$ V_{cc}=600V, V_{GE}=\pm15V, Rg=0.62\Omega, T_j=25^{\circ}C $$$

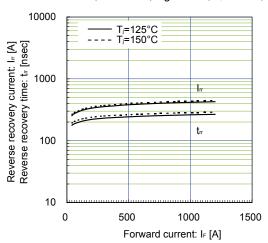


[INVERTER]
Reverse Recovery Characteristics (typ.)
Vcc=600V, Vce=±15V, Rg=0.62Ω, T_i=125°C, 150°C

Forward on voltage: V_F [V]

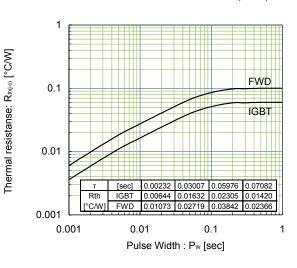
125°C

2

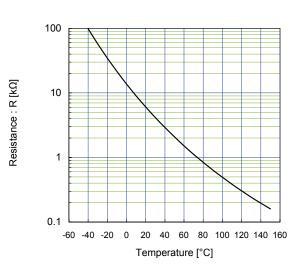


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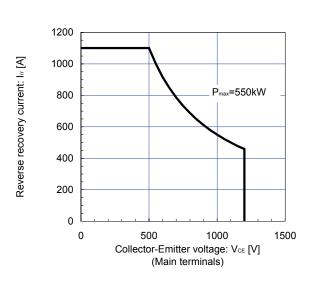
Transient Thermal Resistance (max.)



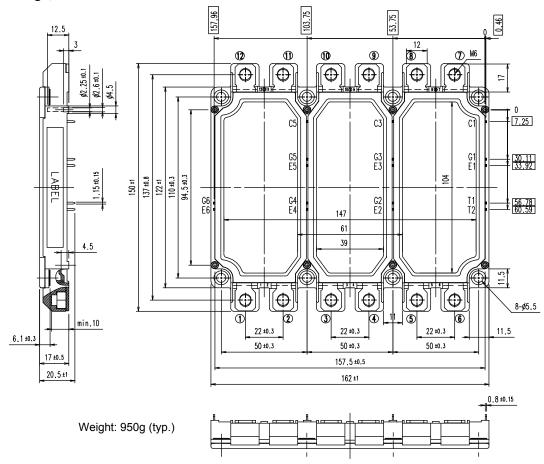
[THERMISTOR]
Temperature characteristic (typ.)



FWD safe operating area (max.) T_i=150°C



■ Outline Drawings, mm



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