

N-CHANNEL SILICON POWER MOSFET

FAP-IIIB SERIES

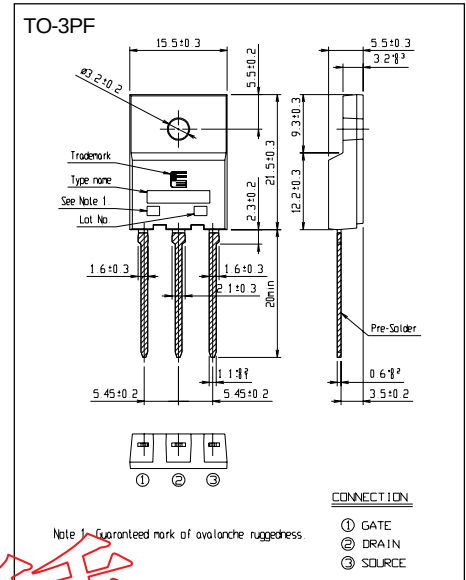
■ Features

- High speed switching
- Low on-resistance
- No secondary breakdown
- Low driving power
- High voltage
- Avalanche-proof

■ Applications

- Switching regulators
- DC-DC converters
- General purpose power amplifier

■ Outline Drawings



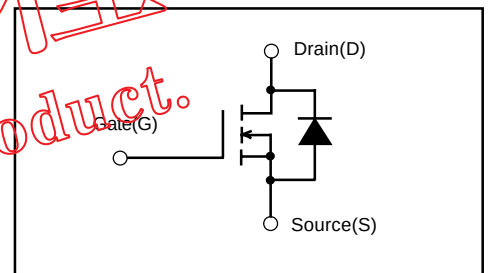
■ Maximum ratings and characteristics

● Absolute maximum ratings ($T_c=25^\circ\text{C}$ unless otherwise specified)

Item	Symbol	Rating	Unit	Remarks
Drain-source voltage	V_{DS}	60	V	
Continuous drain current	I_D	± 70	A	
Pulsed drain current	$I_{D(puls)}$	± 280	A	
Gate-source peak voltage	V_{GS}	± 20	V	
Maximum avalanche energy	E_{AV}	685	mJ	*1
Maximum power dissipation	P_D	100	W	
Operating and storage temperature range	T_{ch} T_{stg}	-55 to $+150$	$^\circ\text{C}$	

*1 $L=0.186\text{mH}$, $V_{CC}=24\text{V}$

■ Equivalent circuit schematic

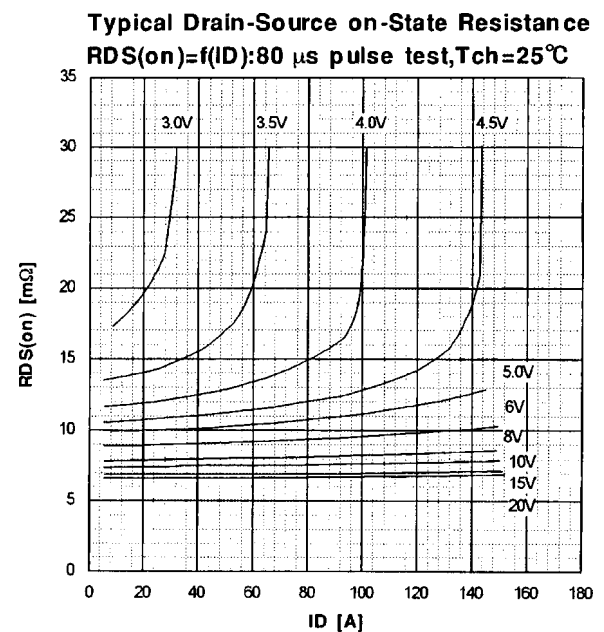
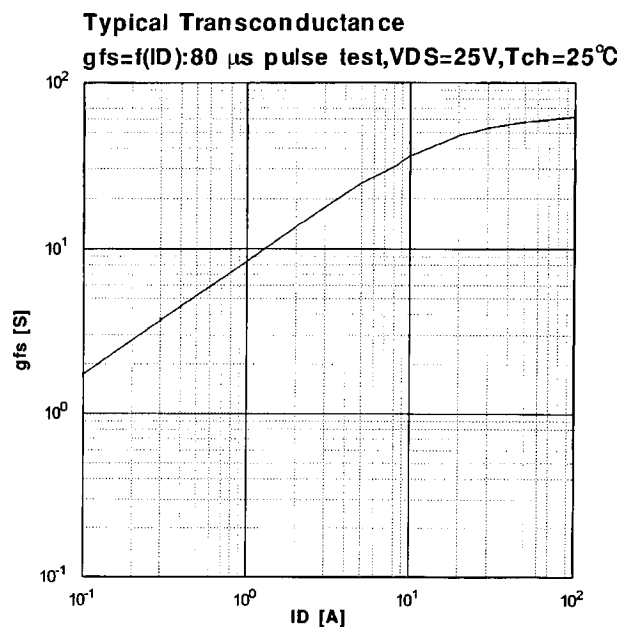
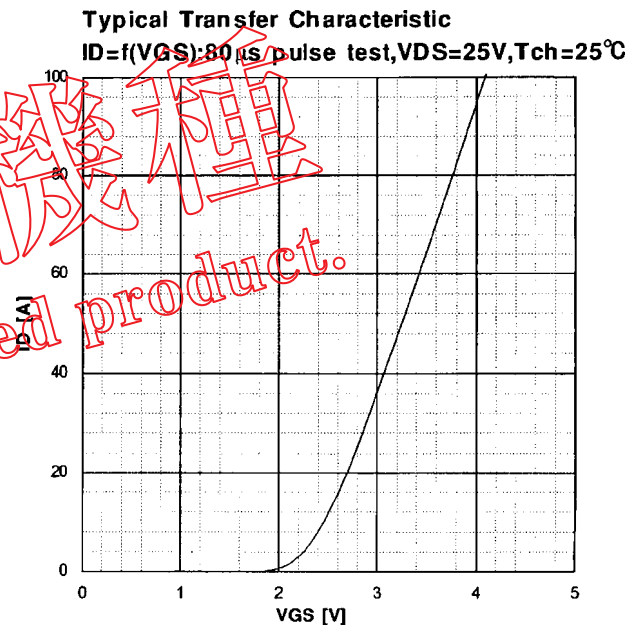
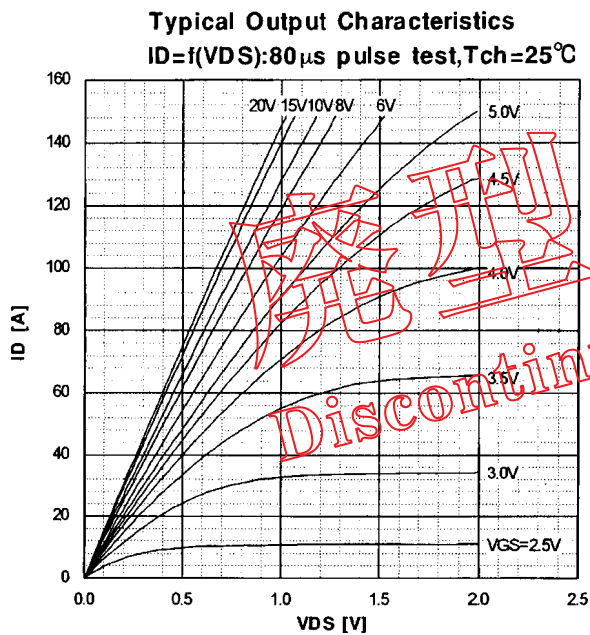
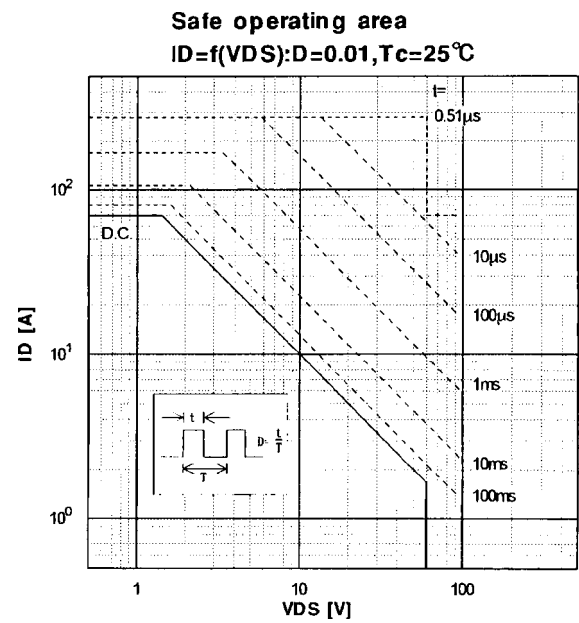
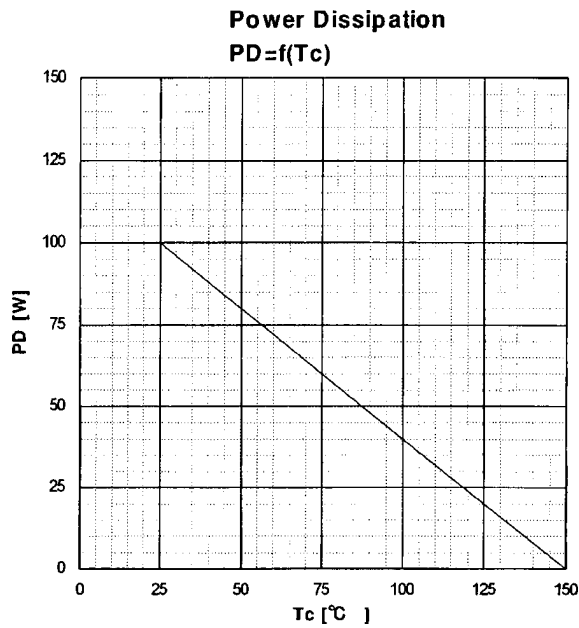
● Electrical characteristics ($T_c=25^\circ\text{C}$ unless otherwise specified)

Item	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Drain-source breakdown voltage	$V_{(BR)DSS}$	$I_D=1\text{mA}$ $V_{GS}=0\text{V}$	60			V
Gate threshold voltage	$V_{GS(th)}$	$I_D=1\text{mA}$ $V_{DS}=V_{GS}$	1.0	1.5	2.0	V
Zero gate voltage drain current	I_{DSS}	$V_{DS}=60\text{V}$ $V_{GS}=0\text{V}$	$T_{ch}=25^\circ\text{C}$	10	500	μA
			$T_{ch}=125^\circ\text{C}$	0.2	1.0	mA
Gate-source leakage current	I_{GSS}	$V_{GS}=20\text{V}$ $V_{DS}=0\text{V}$		10	100	nA
Drain-source on-state resistance	$R_{DS(on)}$	$I_D=40\text{A}$ $V_{GS}=10\text{V}$	$V_{GS}=4\text{V}$	12	17	m Ω
			$V_{GS}=10\text{V}$	7.5	10	m Ω
Forward transconductance	g_{fs}	$I_D=40\text{A}$ $V_{DS}=25\text{V}$	25	55		S
Input capacitance	C_{iss}	$V_{DS}=25\text{V}$		3500	5250	pF
Output capacitance	C_{oss}	$V_{GS}=0\text{V}$		1250	1870	
Reverse transfer capacitance	C_{rss}	$f=1\text{MHz}$		360	540	
Turn-on time	$t_{d(on)}$	$V_{CC}=30\text{V}$ $R_G=10\ \Omega$		15	23	ns
	t_r			75	120	
Turn-off time	$t_{d(off)}$	$V_{GS}=10\text{V}$		190	285	
	t_f			110	165	
Avalanche capability	I_{AV}	$L=100\mu\text{H}$ $T_{ch}=25^\circ\text{C}$	70			A
Diode forward on-voltage	V_{SD}	$I_F=160\text{A}$ $V_{GS}=0\text{V}$ $T_{ch}=25^\circ\text{C}$		1.15	1.65	V
Reverse recovery time	t_{rr}	$I_F=80\text{A}$		75	120	ns
Reverse recovery charge	Q_{rr}	$-di/dt=100\text{A}/\mu\text{s}$ $T_{ch}=25^\circ\text{C}$		0.17		μC

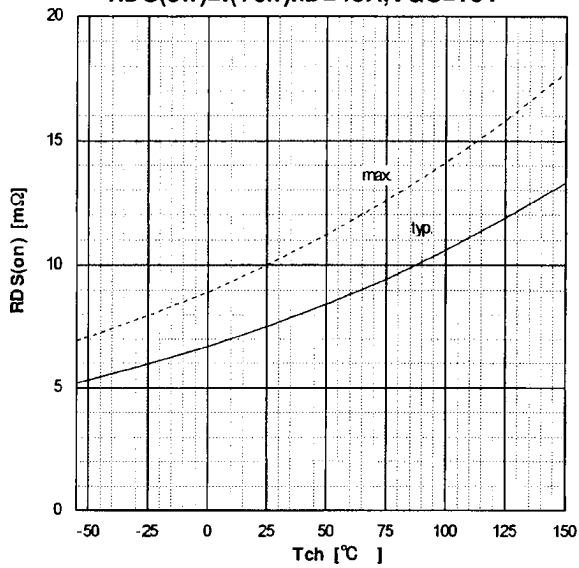
● Thermal characteristics

Item	Symbol	Min.	Typ.	Max.	Units
Thermal resistance	$R_{th(ch-c)}$			1.25	$^\circ\text{C}/\text{W}$
	$R_{th(ch-a)}$			30.0	$^\circ\text{C}/\text{W}$

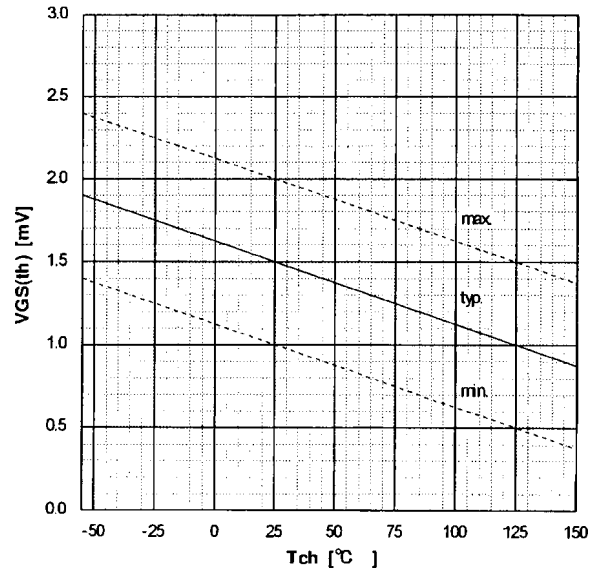
Characteristics



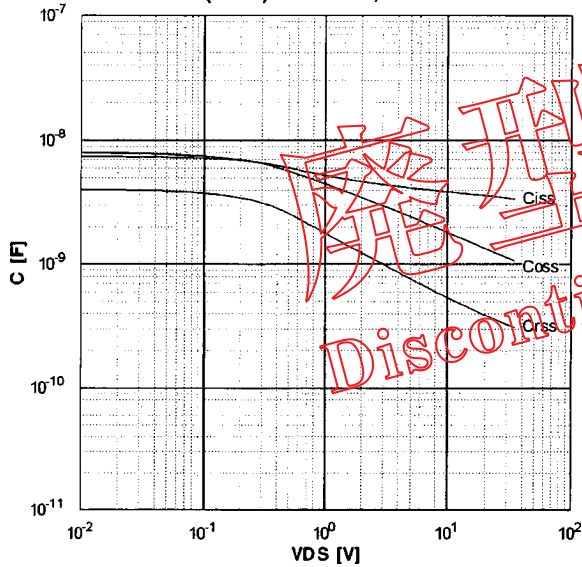
Drain-Source On-state Resistance
 $R_{DS(on)} = f(T_{ch}): I_D = 40A, V_{GS} = 10V$



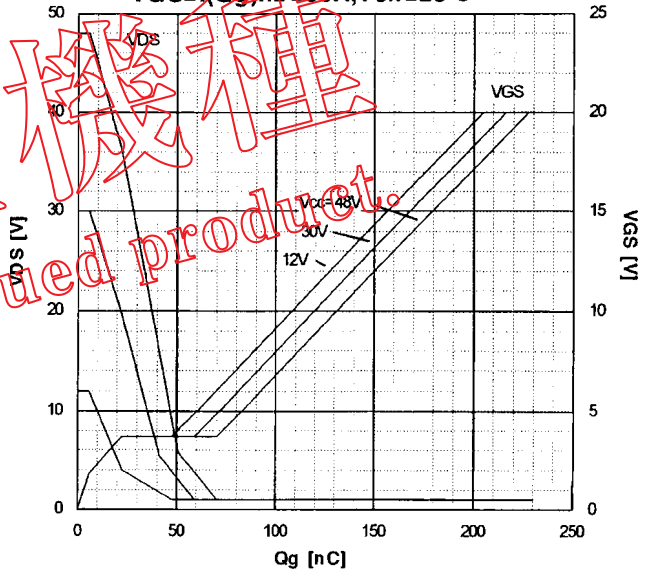
Gate Threshold Voltage vs. Tch
 $V_{GS(th)} = f(T_{ch}): V_{DS} = V_{GS}, I_D = 1mA$



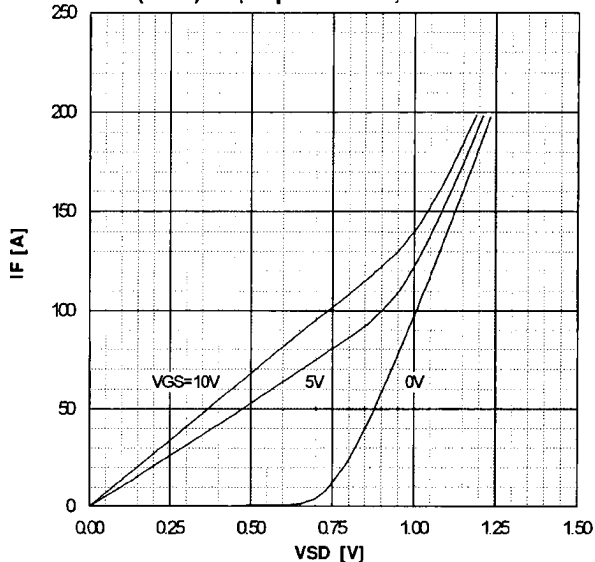
Typical Capacitance
 $C = f(V_{DS}): V_{GS} = 0V, f = 1MHz$



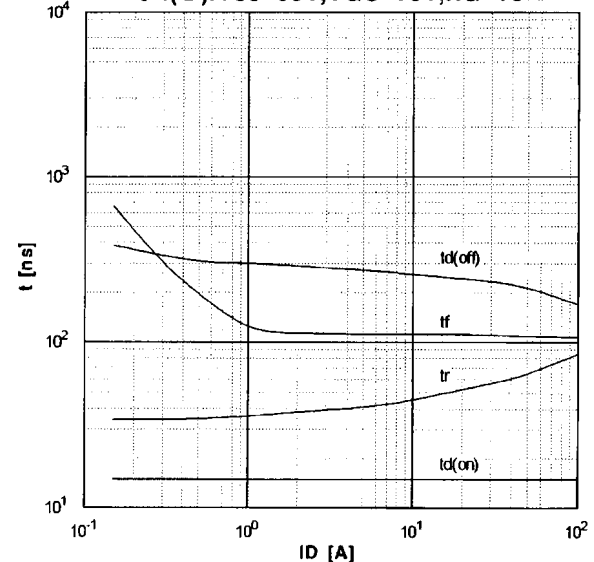
Typical Gate Charge Characteristics
 $V_{GS} = f(Q_g): I_D = 80A, T_{ch} = 25^{\circ}C$



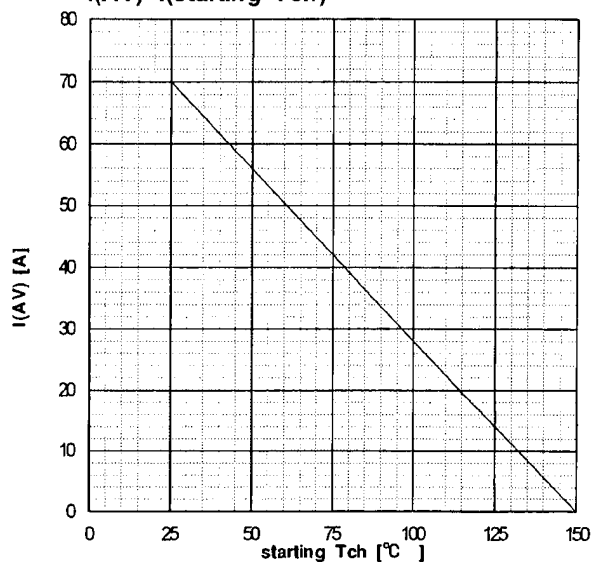
Typical Forward Characteristics of Reverse Diode
 $I_F = f(V_{SD}): 80\mu s \text{ pulse test}, T_{ch} = 25^{\circ}C$



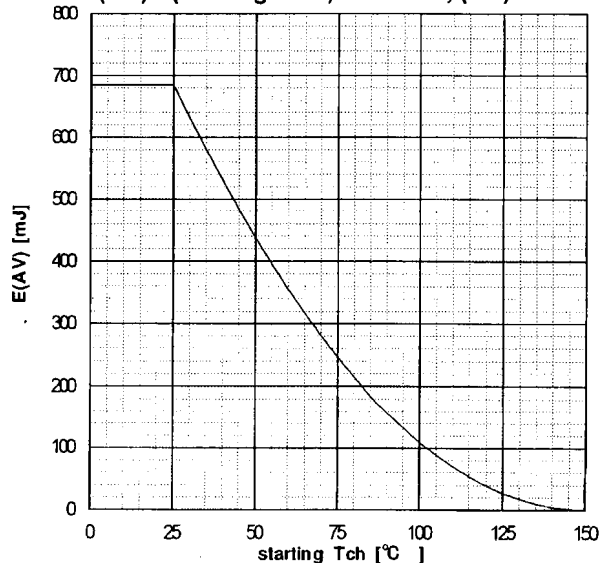
Typical Switching Characteristics vs. I_D
 $t = f(I_D): V_{CC} = 30V, V_{GS} = 10V, R_G = 10\Omega$



Maximum Avalanche Current vs. starting Tch
 $I(AV)=f(\text{starting Tch})$



Maximum Avalanche Energy vs. starting Tch
 $E(AV)=f(\text{starting Tch}): V_{CC}=24V, I(AV) \leq 70A$



Transient Thermal Impedance

$Z_{th(ch-c)}=f(t)$ parameter: $D=1/7$

