

FMH28N50ES

FUJI POWER MOSFET

Super FAP-E^{3S} series

N-CHANNEL SILICON POWER MOSFET

Features

Maintains both low power loss and low noise Lower R_{DS}(on) characteristic More controllable switching dv/dt by gate resistance

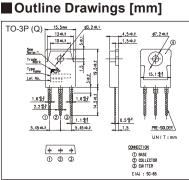
Smaller V_{GS} ringing waveform during switching Narrow band of the gate threshold voltage $(4.2\pm0.5V)$ High avalanche durability

Applications

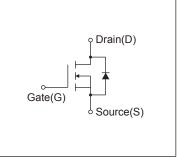
Switching regulators UPS (Uninterruptible Power Supply) DC-DC converters

Maximum Ratings and Characteristics

• Absolute Maximum Ratings at Tc=25°C (unless otherwise specified)



Equivalent circuit schematic



Description	Symbol	Characteristics	Unit	Remarks
Durin Origina Vieldana	VDS	500	V	
Drain-Source Voltage	VDSX	500	V	V _{GS} = -30V
Continuous Drain Current	lo	±28	А	
Pulsed Drain Current	IDP	±112	А	
Gate-Source Voltage	Vgs	±30	V	
Repetitive and Non-Repetitive Maximum Avalanche Current	lar	28	А	Note*1
Non-Repetitive Maximum Avalanche Energy	Eas	1033.1	mJ	Note*2
Repetitive Maximum Avalanche Energy	Ear	40	mJ	Note*3
Peak Diode Recovery dV/dt	dV/dt	6.9	kV/µs	Note*4
Peak Diode Recovery -di/dt	-di/dt	100	A/µs	Note*5
	Po	2.50	14/	Ta=25°C
Maximum Power Dissipation		400	W	Tc=25°C
On another and Otamana Tananantana manana	Tch	150	°C	
Operating and Storage Temperature range	Tstg	-55 to + 150	°C	

• Electrical Characteristics at Tc=25°C (unless otherwise specified)

Description	Symbol	Conditions		min.	typ.	max.	Unit
Drain-Source Breakdown Voltage	BVDSS	ID=250µA, VGS=0V		500	-	-	V
Gate Threshold Voltage	Vgs (th)	ID=250µA, VDS=VGS	ID=250µA, VDS=VGS		4.2	4.7	V
Zero Gate Voltage Drain Current	Ipss	V _{DS} =500V, V _{GS} =0V	Tch=25°C	-	-	25	μA
	IDSS	V _{DS} =400V, V _{GS} =0V	Tch=125°C	-	-	250	
Gate-Source Leakage Current	Igss	V _{GS} =±30V, V _{DS} =0V		-	10	100	nA
Drain-Source On-State Resistance	RDS (on)	I _D =14A, V _{GS} =10V		-	0.16	0.19	Ω
Forward Transconductance	g fs	ID=14A, VDS=25V		10.5	21	-	S
Input Capacitance	Ciss	V _{DS} =25V V _{GS} =0V		-	3500	5250	pF
Output Capacitance	Coss			-	420	630	
Reverse Transfer Capacitance	Crss	f=1MHz	-	24	36		
Turn-On Time	td(on)	V _{cc} =300V V _{GS} =10V I _D =14A R _{GS} =8.2Ω		-	45	67.5	ns
	tr			-	40	60	
Turn-Off Time	td(off)			-	107	160.5	
	tf			-	17	25.5	
Total Gate Charge	QG	- V _{cc} =250V I _D =28A V _{GS} =10V		-	92	138	nC
Gate-Source Charge	QGS			-	30	45	
Gate-Drain Charge	QGD			-	34	51	
Gate-Drain Crossover Charge	Qsw			-	13	19.5	
Avalanche Capability	lav	L=1.04mH, T _{ch} =25°C		28	-	-	А
Diode Forward On-Voltage	Vsd	IF=28A, VGS=0V, Tch=25°C		-	0.90	1.35	V
Reverse Recovery Time	trr	I⊧=28A, V₀₅=0V -di/dt=100A/μs, Tch=25°C		-	0.72	-	μs
Reverse Recovery Charge	Qrr			-	11.2	-	μC

Thermal Characteristics

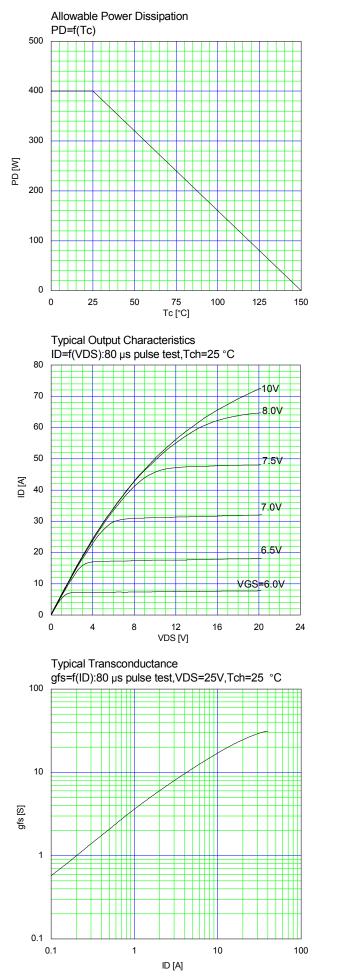
Description	Symbol	Test Conditions	min.	typ.	max.	Unit
Thermal resistance	Rth (ch-c)	Channel to Case			0.313	°C/W
Thermal resistance	Rth (ch-a)	Channel to Ambient			50.0	°C/W

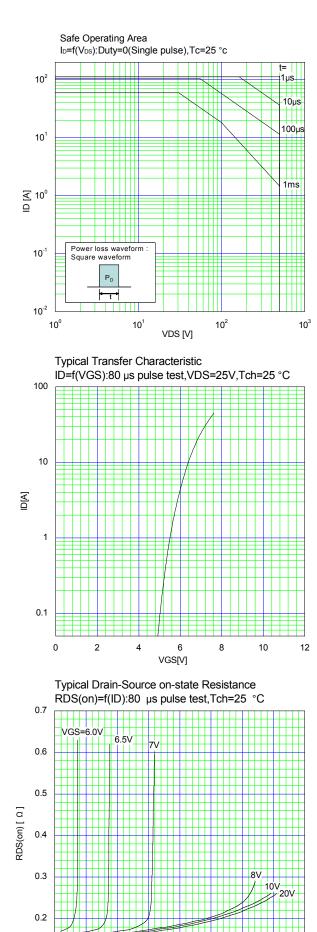
Note *1 : Tch≤150°C.

Note *2 : Stating Tch=25°C, IAs=12A, L=13.2mH, Vcc=50V, Rg=50Ω. EAs limited by maximum channel temperature and avalanche current. See to 'Avalanche Energy' graph. Note *3 : Repetitive rating : Pulse width limited by maximum channel temperature.

See to the 'Transient Themal impeadance' graph. Note *4 : IFS-ID, -di/dt=100A/µs, Vcc≤BVDss, Tch≤150°C.

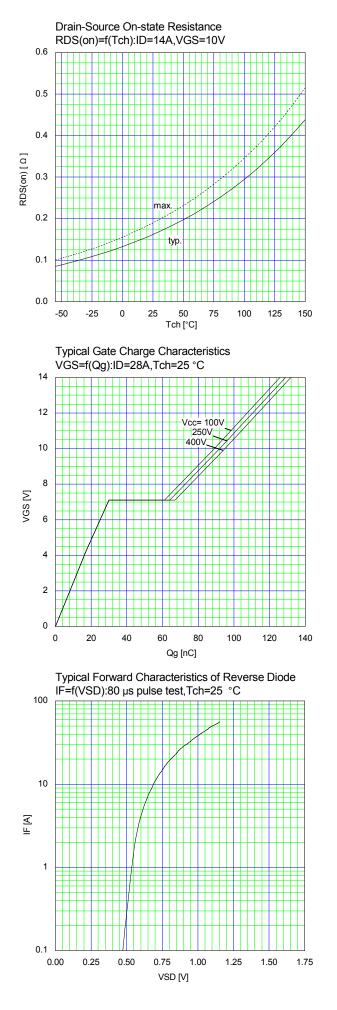
Note *5 : I⊧≤-ID, dv/dt=6.9kV/µs, Vcc≤BVDss, Tch≤150°C.

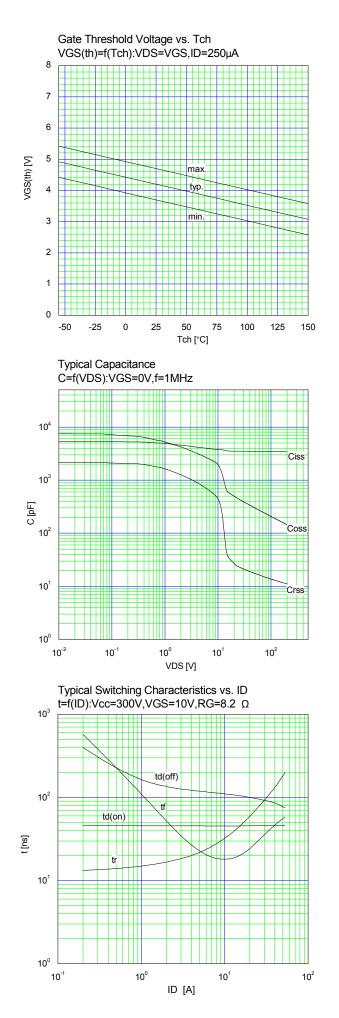


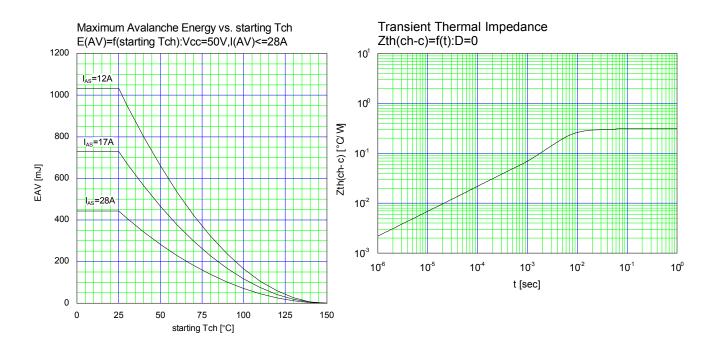


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