

FMI11N60E

FUJI POWER MOSFET

Super FAP-E³ series

N-CHANNEL SILICON POWER MOSFET

■ Features

Maintains both low power loss and low noise Lower R_{DS}(on) characteristic More controllable switching dv/dt by gate resistance Smaller V_{GS} ringing waveform during switching Narrow band of the gate threshold voltage (3.0±0.5V) High avalanche durability

Applications

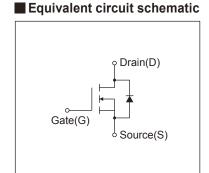
Switching regulators UPS (Uninterruptible Power Supply) DC-DC converters

Maximum Ratings and Characteristics

● Absolute Maximum Ratings at Tc=25°C (unless otherwise specified)

1.2+0.2 Solder Hill Hill Hill Hill Hill Hill Hill Hil	T-Pack(L) Irodemark Type name Lot No.	3.6	4,510,2 1,310,2 Fig. 1.	Fig. 1. (See Notes)
	Φ	0.837		-
ORMECTION	+	T T		D GATE 2) DRAIN

■ Outline Drawings [mm]



Description	Symbol	Characteristics	Unit	Remarks	
Proin Source Veltore	V _{DS}	600	V		
Drain-Source Voltage	V _{DSX}	600	V	V _{GS} = -30V	
Continuous Drain Current	Io	±11	Α		
Pulsed Drain Current	IDP	±44	Α		
Gate-Source Voltage	V _G s	±30	V		
Repetitive and Non-Repetitive Maximum AvalancheCurrent	Iar	11	Α	Note*1	
Non-Repetitive Maximum Avalanche Energy	Eas	384	mJ	Note*2	
Repetitive Maximum Avalanche Energy	Ear	18.0	mJ	Note*3	
Peak Diode Recovery dV/dt	dV/dt	4.9	kV/μs	Note*4	
Peak Diode Recovery -di/dt	-di/dt	100	A/µs	Note*5	
Maniana Barra Birainatian	P₀	1.67	10/	Ta=25°C	
Maximum Power Dissipation		180	W	Tc=25°C	
O	Tch	150	°C		
Operating and Storage Temperature range	Tstg	-55 to + 150	°C		

● Electrical Characteristics at Tc=25°C (unless otherwise specified)

Description	Symbol	Conditions		min.	typ.	max.	Unit
Drain-Source Breakdown Voltage	BVDSS	I _D =250μA, V _{GS} =0V		600	-	-	V
Gate Threshold Voltage	V _{GS} (th)	In=250µA, Vos=Vos	I _D =250µA, V _{DS} =V _{GS}		3.0	3.5	V
Zero Gate Voltage Drain Current		V _{DS} =600V, V _{GS} =0V	T _{ch} =25°C	-	-	25	μА
	IDSS	V _{DS} =480V, V _{GS} =0V	T _{ch} =125°C	-	-	250	
Gate-Source Leakage Current	Igss	V _{GS} =±30V, V _{DS} =0V	V _{GS} =±30V, V _{DS} =0V		10	100	nA
Drain-Source On-State Resistance	R _{DS} (on)	I _D =5.5A, V _{GS} =10V		-	0.64	0.75	Ω
Forward Transconductance	g fs	I _D =5.5A, V _{DS} =25V		6	12	-	S
Input Capacitance	Ciss	V _{DS} =25V V _{GS} =0V		-	1700	2550	pF
Output Capacitance	Coss			-	150	225	
Reverse Transfer Capacitance	Crss	f=1MHz		-	11	16.5	
Town On Time	td(on)	Vcc=300V		-	21	31.5	200
Turn-On Time	tr	V _{GS} =10V		-	9.5	14.3	
T Off Time .	td(off)	ID=5.5A			100	150	ns
Turn-Off Time	tf	R _G =15Ω		-	19	28.5	
Total Gate Charge	Q _G	V _{cc} =300V I _D =11A V _{GS} =10V		-	48.5	73	nC
Gate-Source Charge	QGS			-	12.5	19	
Gate-Drain Charge	Q _{GD}			-	14	21	
Avalanche Capability	lav	L=2.64mH, T _{ch} =25°C		11	-	-	Α
Diode Forward On-Voltage	V _{SD}	I _F =11A, V _{GS} =0V, T _{ch} =25°	C	-	0.86	1.30	V
Reverse Recovery Time	trr	I _F =11A, V _{GS} =0V		-	0.52	-	μS
Reverse Recovery Charge	Qrr	-di/dt=100A/µs, Tch=25°C		-	5.5	-	μC

Thermal Characteristics

Description	Symbol	Test Conditions	min.	typ.	max.	Unit
Thermal resistance	Rth (ch-c)	Channel to Case			0.690	°C/W
	Rth (ch-a)	Channel to Ambient			75.0	°C/W

Note *1 : Tch≤150°C

Note 12: Stating Tch=25°C, Ias=5A, L=28.2mH, Vcc=60V, R_G=50Ω

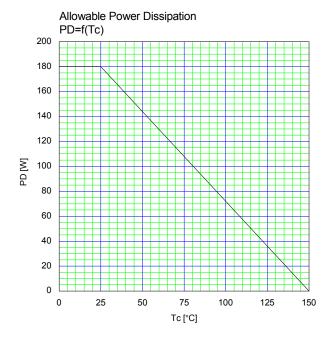
Eas limited by maximum channel temperature and avalanche current.

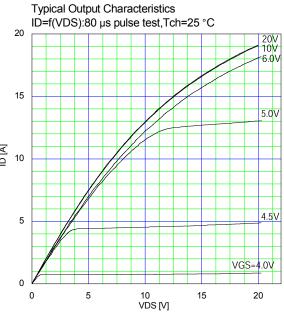
See to 'Avalanche Energy' graph.

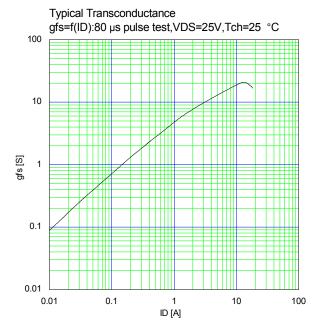
Note $^{\star}3$: Repetitive rating : Pulse width limited by maximum channel temperature

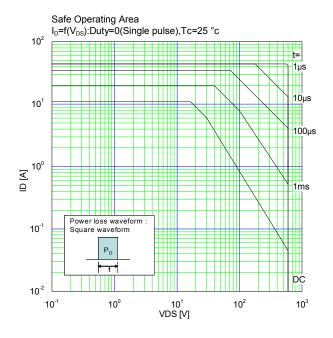
See to the 'Transient Themal impeadance' graph. Note *4 : IF<-Ip, -di/dt=100A/ μ s, Vcc≤BV $_{DSS}$, Tch≤150°C.

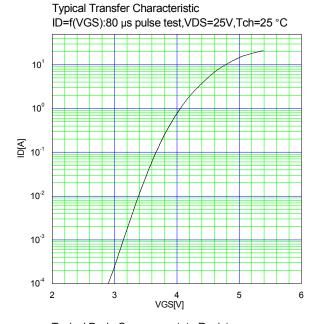
Note *5 : Ir≤-ID, dv/dt=4.4kV/µs, Vcc≤BVbss, Tch≤150°C.

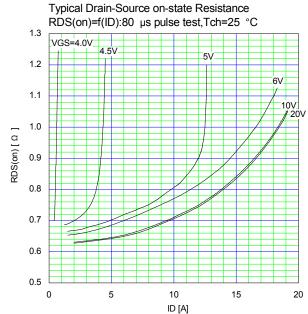




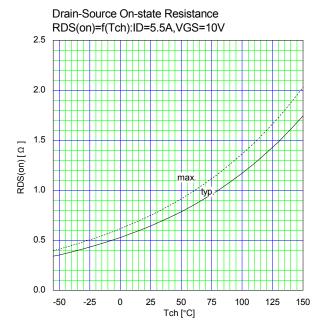


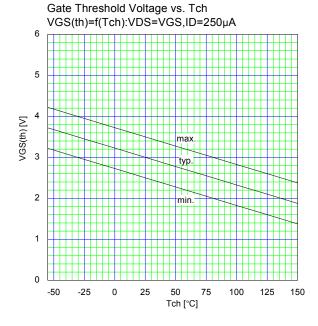


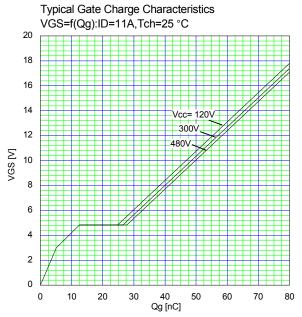


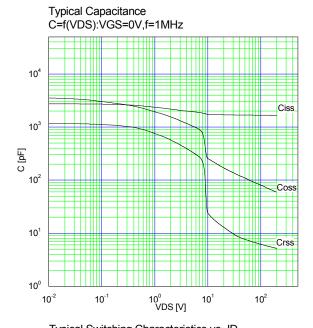


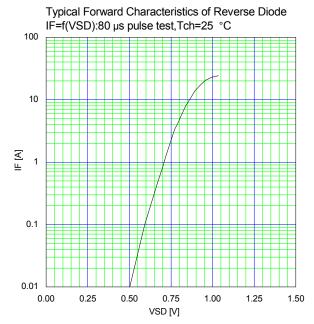
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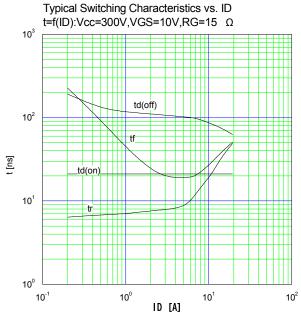


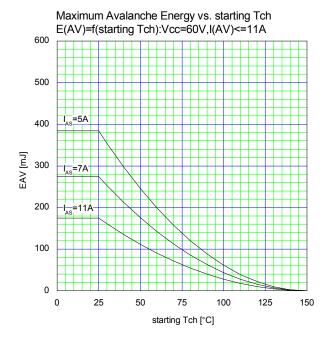


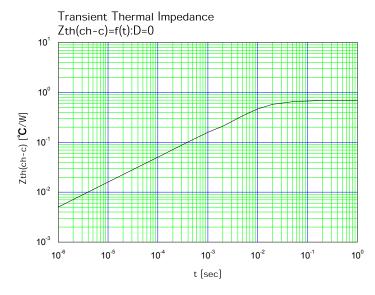












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