

# FML20N50ES

#### **FUJI POWER MOSFET**

## Super FAP-E<sup>3</sup> series

### N-CHANNEL SILICON POWER MOSFET

#### Features

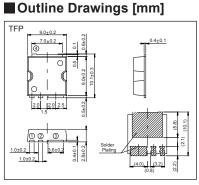
Maintains both low power loss and low noise Lower R<sub>DS</sub>(on) characteristic More controllable switching dv/dt by gate resistance Smaller V<sub>GS</sub> ringing waveform during switching Narrow band of the gate threshold voltage (4.2±0.5V) High avalanche durability

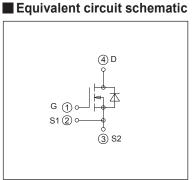
#### Applications

Switching regulators UPS (Uninterruptible Power Supply) **DC-DC converters** 

#### Maximum Ratings and Characteristics

#### Absolute Maximum Ratings at Tc=25°C (unless otherwise specified)





Description	Symbol	Characteristics	Unit	Remarks
Drain Course Voltage	VDS	500	V	
Drain-Source Voltage	VDSX	500	V	V <sub>GS</sub> = -30V
Continuous Drain Current	lo	±20	А	
Pulsed Drain Current	IDP	±80	А	
Gate-Source Voltage	Vgs	±30	V	
Repetitive and Non-Repetitive Maximum Avalanche Current	lar	20	А	Note*1
Non-Repetitive Maximum Avalanche Energy	Eas	582.5	mJ	Note*2
Repetitive Maximum Avalanche Energy	Ear	9.5	mJ	Note*3
Peak Diode Recovery dV/dt	dV/dt	4.6	kV/µs	Note*4
Peak Diode Recovery -di/dt	-di/dt	100	A/µs	Note*5
Maulaum Daura Diagliagé	PD	2.16	W	Ta=25°C
Maximum Power Dissipation		95	VV	Tc=25°C
On and the sead Othersen Terror sectors are set	Tch	150	°C	
Operating and Storage Temperature range	Tstg	-55 to + 150	°C	
Isolation Voltage	Viso	2	kVrms	t = 60sec, f = 60Hz

#### Electrical Characteristics at Tc=25°C (unless otherwise specified)

Description	Symbol	Conditions	Conditions		typ.	max.	Unit	
Drain-Source Breakdown Voltage	BVDSS	I <sub>D</sub> =250µA, V <sub>GS</sub> =0V		500	-	-	V	
Gate Threshold Voltage	Vgs (th)	ID=250µA, VDS=VGS	ID=250µA, VDS=VGS		4.2	4.7	V	
Zero Gate Voltage Drain Current		V <sub>DS</sub> =500V, V <sub>GS</sub> =0V	T <sub>ch</sub> =25°C	-	-	25		
	IDSS	V <sub>DS</sub> =400V, V <sub>GS</sub> =0V	Tch=125°C	-	-	250	- μΑ	
Gate-Source Leakage Current	Igss	V <sub>GS</sub> =±30V, V <sub>DS</sub> =0V	V <sub>GS</sub> =±30V, V <sub>DS</sub> =0V		10	100	nA	
Drain-Source On-State Resistance	RDS (ON)	ID=10A, VGS=10V	I <sub>D</sub> =10A, V <sub>GS</sub> =10V		0.27	0.31	Ω	
Forward Transconductance	g <sub>fs</sub>	ID=10A, VDS=25V	ID=10A, VDS=25V		10	-	S	
Input Capacitance	Ciss	V <sub>DS</sub> =25V		-	2100	3150	pF	
Output Capacitance	Coss	V <sub>GS</sub> =0V	V <sub>GS</sub> =0V		250	375		
Reverse Transfer Capacitance	Crss	f=1MHz		-	15	22.5		
Turn-On Time	td(on)	Vcc=300V		-	40	60	ns	
	tr	V <sub>GS</sub> =10V		-	38	57		
Turn-Off Time	td(off)	ID=10A		-	85	127.5		
	tf	R <sub>GS</sub> =15Ω		-	17	25.5		
Total Gate Charge	QG	V <sub>cc</sub> =250V I <sub>D</sub> =20A V <sub>GS</sub> =10V		-	57	85.5	nC	
Gate-Source Charge	Q <sub>GS</sub>			-	21	31.5		
Gate-Drain Charge	QGD			-	21	31.5		
Gate-Drain Crossover Charge	Qsw			-	10	15		
Avalanche Capability	lav	L=1.07mH, T <sub>ch</sub> =25°C		20	-	-	A	
Diode Forward On-Voltage	Vsd	IF=20A, VGS=0V, Tch=25°C		-	0.90	1.35	V	
Reverse Recovery Time	trr	I <sub>F</sub> =20A, V <sub>GS</sub> =0V		-	0.5	-	μs	
Reverse Recovery Charge	Qrr	-di/dt=100A/µs, Tch=25°C		-	7.0	-	μC	

#### Thermal Characteristics

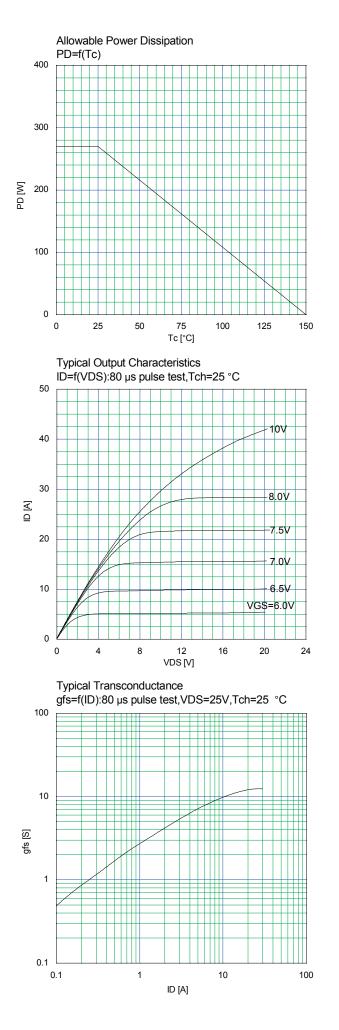
Description	Symbol	Test Conditions	min.	typ.	max.	Unit
Thermal resistance	Rth (ch-c)	Channel to Case			1.320	°C/W
	Rth (ch-a)	Channel to Ambient			58.0	°C/W

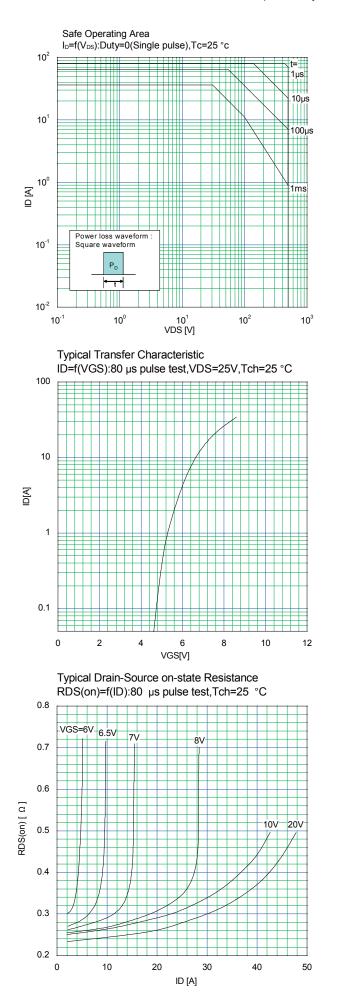
Note \*1 : Tch≤150°C.

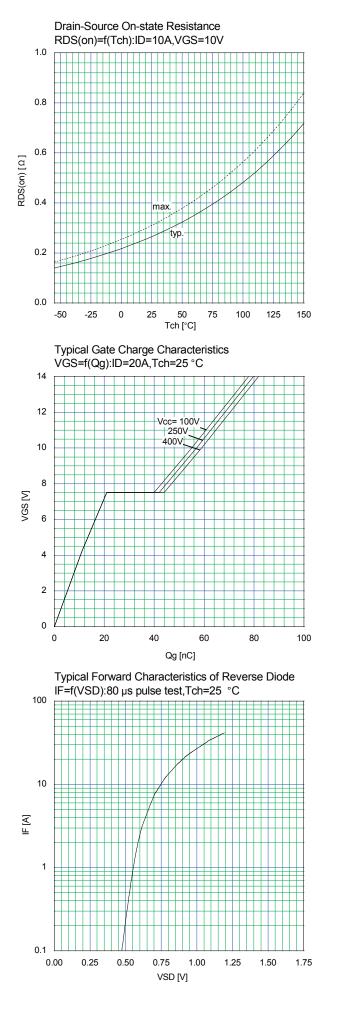
Note '1 : Stating Tch-25°C, IAs=8A, L=16.7mH, Vcc=50V, Re=50Ω. EAs limited by maximum channel temperature and avalanche current. See to 'Avalanche Energy' graph.

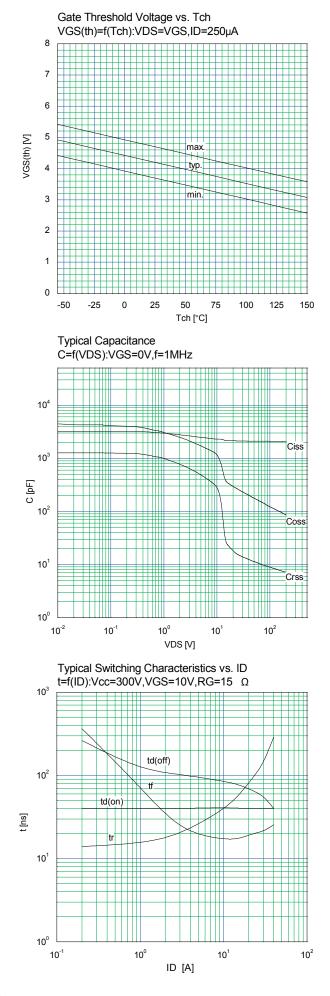
Note \*3 : Repetitive rating : Pulse width limited by maximum channel temperature.

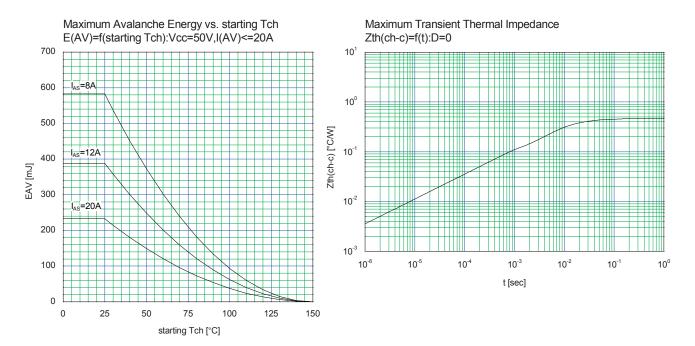
See to the 'Transient Themal impeadance' graph. Note \*4 : I⊧S-ID, -di/dt=100A/µs, Vcc≤BVbss, Tch≤150°C. Note \*5 : I⊧S-ID, dv/dt=4.6kV/µs, Vcc≤BVbss, Tch≤150°C.











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