

FMV10N60E

FUJI POWER MOSFET

Super FAP-E³ series

N-CHANNEL SILICON POWER MOSFET

■ Features

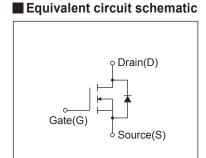
Maintains both low power loss and low noise Lower $R_{DS}(on)$ characteristic More controllable switching dv/dt by gate resistance Smaller V_{GS} ringing waveform during switching Narrow band of the gate threshold voltage (3.0±0.5V) High avalanche durability

Applications

Switching regulators
UPS (Uninterruptible Power Supply)
DC-DC converters

■ Maximum Ratings and Characteristics

● Absolute Maximum Ratings at Tc=25°C (unless otherwise specified)



Symbol Description Characteristics Unit Remarks V_{DS} **Drain-Source Voltage** V_{GS} = -30V VDSX 600 V **Continuous Drain Current** ΙD ±10 Α **Pulsed Drain Current** IDP ±40 Α Gate-Source Voltage Vgs ±30 Repetitive and Non-Repetitive Maximum AvalancheCurrent I_{AR} 10 Α Note*1 Non-Repetitive Maximum Avalanche Energy 416 Note*2 EAS mJ Repetitive Maximum Avalanche Energy E_{AR} 6.0 mJ Note*3 Peak Diode Recovery dV/dt dV/dt Note*4 44 kV/us Peak Diode Recovery -di/dt -di/dt 100 Note*5 A/µs 2.16 Ta=25°C **Maximum Power Dissipation** P_{D} W 60 Tc=25°C Tch 150 °C **Operating and Storage Temperature range** Tstg -55 to + 150 °C Isolation Voltage t = 60sec, f = 60Hz kVrms Viso 2

● Electrical Characteristics at Tc=25°C (unless otherwise specified)

Description	Symbol	Conditions		min.	typ.	max.	Unit
Drain-Source Breakdown Voltage	BVDSS	I _D =250μA, V _{GS} =0V		600	-	-	V
Gate Threshold Voltage	V _{GS} (th)	I _D =250µA, V _{DS} =V _{GS}		2.5	3.0	3.5	V
Zero Gate Voltage Drain Current		V _{DS} =600V, V _{GS} =0V	Tch=25°C	-	-	25	μΑ
	IDSS	V _{DS} =480V, V _{GS} =0V	T _{ch} =125°C	-	-	250	
Gate-Source Leakage Current	Igss	V _{GS} =±30V, V _{DS} =0V		-	10	100	nA
Drain-Source On-State Resistance	Ros (on)	I _D =5A, V _{GS} =10V		-	0.675	0.79	Ω
Forward Transconductance	g fs	I _D =5A, V _{DS} =25V		6	12	-	S
Input Capacitance	Ciss	V _{DS} =25V V _{GS} =0V f=1MHz		-	1800	2700	pF
Output Capacitance	Coss			-	140	210	
Reverse Transfer Capacitance	Crss			-	10.5	16	
Turn-On Time	td(on)	V _{cc} =300V V _{ds} =10V I _D =5A R ₆ =15Ω		-	20	30	ns
	tr			-	9	13.5	
Turn-Off Time	td(off)			-	100	150	
	tf			-	18	27	
Total Gate Charge	Q _G	V _{cc} =300V I _D =10A V _{cs} =10V		-	47	70.5	nC
Gate-Source Charge	QGS			-	10.5	16	
Gate-Drain Charge	Q _{GD}			-	13.5	20	
Avalanche Capability	lav	L=3.05mH, T _{ch} =25°C		10	-	-	Α
Diode Forward On-Voltage	V _{SD}	I _F =10A, V _{GS} =0V, T _{ch} =25°C		-	0.86	1.30	V
Reverse Recovery Time	trr	I _F =10A, V _{GS} =0V		-	0.51	-	μS
Reverse Recovery Charge	Qrr	-di/dt=100A/µs, Tch=25°C		-	5.4	-	μC

Thermal Characteristics

Description	Symbol	Test Conditions	min.	typ.	max.	Unit
Thermal resistance	Rth (ch-c)	Channel to Case			2.083	°C/W
	Rth (ch-a)	Channel to Ambient			58.0	°C/W

Note *1 : Tch≤150°C

Note *2 : Stating Tch=25°C, Ias=4A, L=47.7mH, Vcc=60V, R_G=50Ω

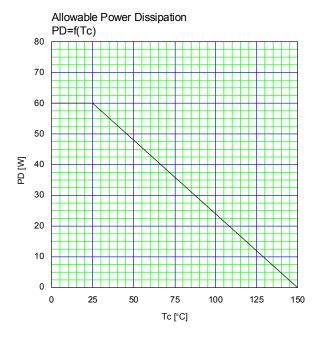
E_{AS} limited by maximum channel temperature and avalanche current.

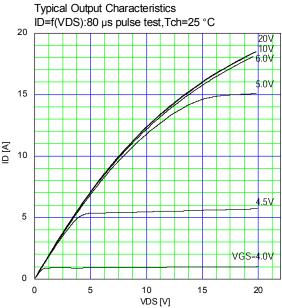
See to 'Avalanche Energy' graph.

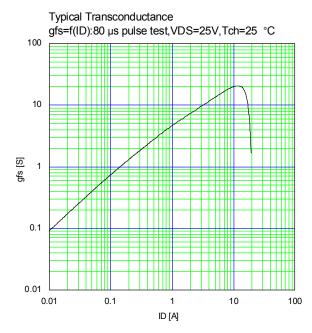
Note *3 : Repetitive rating : Pulse width limited by maximum channel temperature.

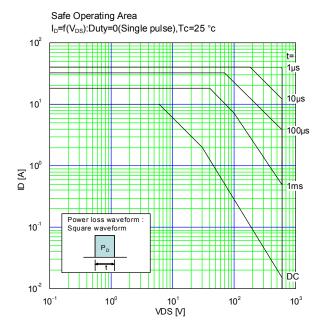
See to the 'Transient Themal impeadance' graph. Note *4 : IFS-ID, -di/dt=100A/µs, Vcc≤BVDss, Tch≤150°C.

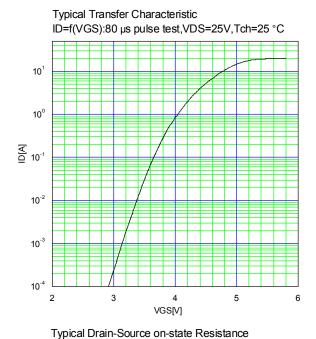
Note *5 : IF≤-ID, dv/dt=4.4kV/µs, Vcc≤BVbss, Tch≤150°C.

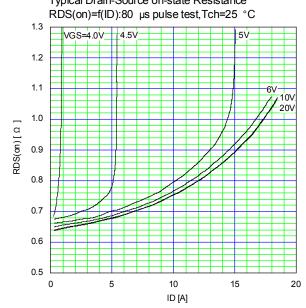




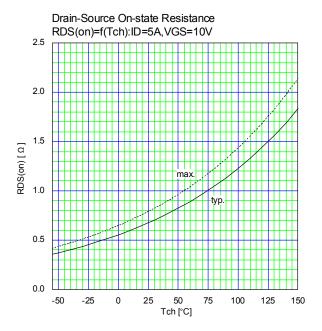


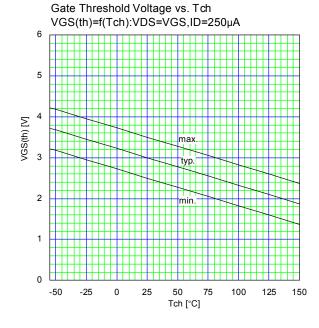


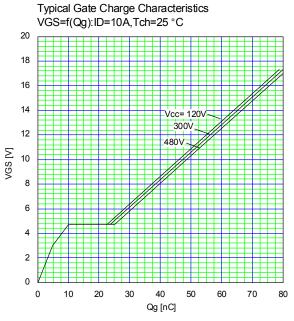


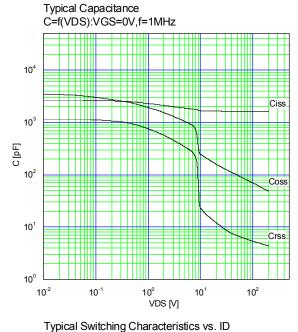


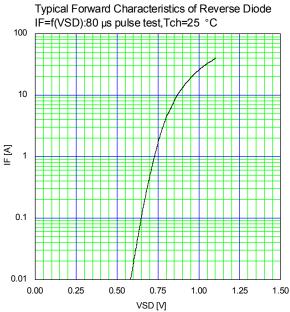
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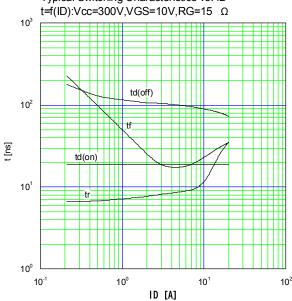


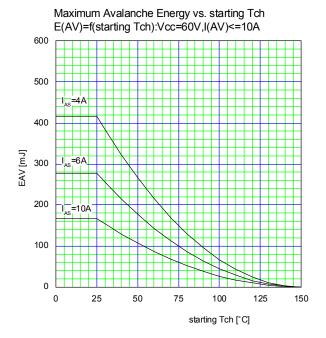


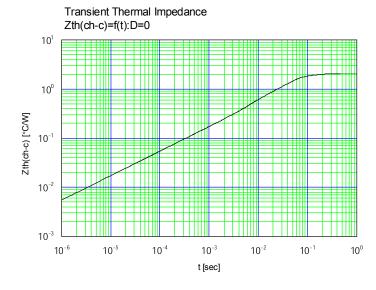












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