

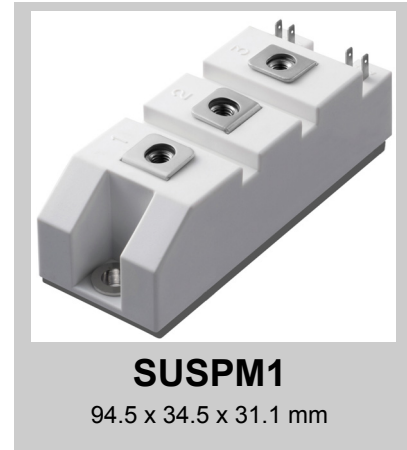
Features

- Soft Punch Through (SPT⁺) Technology
 - Low Loss
 - Highly rugged SPT⁺ design
- Free Wheeling Diodes with soft reverse recovery
- Industrial standard package with copper base plate
- Included ESD protection function ^{*(1)}

Applications

- Welder / Power Supply
- UPS / Inverter
- Industrial Motor Driver

Preliminary data

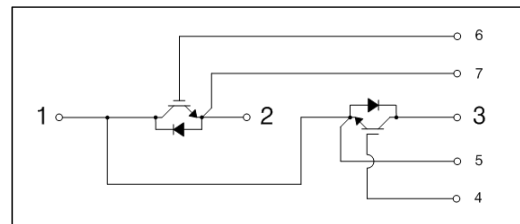


Absolute Maximum Ratings T_C = 25°C unless otherwise noted

Item	Symbol	Conditions	Value	Units
IGBT	V _{CES}		1200	V
	V _{GES}		± 20	V
	I _C	@T _J = 150 °C, T _C = 25 °C, Continuous	100	A
		@T _J = 150 °C, T _C = 80 °C, Continuous	75	A
	I _{CM}	@T _C = 80 °C, t _p = 1 ms	150	A
	T _{SC}	Chip Level, @T _J = 125 °C, V _{GE} = 15 V, V _{CES} < 1200 V	10	µs
	T _J	Operating Junction Temperature ^{*(2)}	-40~125	°C
	P _D	@T _J = 150 °C, T _C = 25 °C	500	W
@T _J = 150 °C, T _C = 80 °C		250	W	
Diode	V _{RRM}		1200	V
	I _F	@T _J = 150 °C, T _C = 80 °C, Continuous	75	A
	I _{FRM}	t _p = 1 ms	150	A
	T _J	Operating Junction Temperature ^{*(2)}	-40~125	°C
Module	T _{stg}	Storage Temperature	-40~125	°C
	V _{iso}	@ AC 1minute	2500	V
	M _t	Main Terminal Mounting torque (M5)	2.5~5	Nm
	M _S	Heat sink Mounting torque (M6)	3.0~5	Nm
	W	Weight	180	g

Internal Circuit & Pin Description

Pin Number	Pin Name	Pin Description
1	C2E1	Output
2	E2	Negative DC Link Output
3	C1	Positive DC Link Output
4	G1	Gate Input for High-side
5	E1	Emitter Input for High-side
6	G2	Gate Input for Low-side
7	E2	Emitter Input for Low-side



(Note *1) Option : Included ± 28 V Zener Diode between Gate and Emitter.
 (Note *2) The Maximum junction temperature of chip is 150 °C.

Electrical Characteristics of IGBT and Diode $T_C = 25^\circ\text{C}$ unless otherwise noted

Static Characteristics

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
BV_{CES}	C-E Breakdown Voltage	$V_{GE} = 0\text{ V}, I_C = 1\text{ mA}$	1200	-	-	V
I_{CES}	C-E Cut-Off Current	$V_{CE} = V_{CES}, V_{GE} = 0\text{ V}$	-	-	1	mA
I_{GES}	G-E Leakage Current	$V_{GE} = V_{GES}, V_{CE} = 0\text{ V}$	-	-	-	nA
$V_{GE(th)}$	G-E Threshold Voltage	$V_{GE} = V_{CE}, I_C = 75\text{ mA}$	-	6.9	-	V
$V_{CE(sat)}$	Collector to Emitter Saturation Voltage	$I_C = 75\text{ A}, V_{GE} = 15\text{ V}, T_C = 25^\circ\text{C}$	-	1.9	-	V
		$I_C = 75\text{ A}, V_{GE} = 15\text{ V}, T_C = 125^\circ\text{C}$	-	2.2	-	V

Dynamic Characteristics

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
C_{ies}	Input Capacitance	$V_{CE} = 25\text{ V}, V_{GE} = 0\text{ V}$ $f = 1\text{ MHz}, T_C = 25^\circ\text{C}$	-	5.5	-	nF
C_{oes}	Output Capacitance		-	0.4	-	nF
C_{res}	Reverse Transfer Capacitance		-	0.3	-	nF
$t_d(on)$	Turn-On Delay Time	$T_C = 125^\circ\text{C}, R_G = 15\ \Omega$ $L = 100\ \mu\text{H}, V_{DC} = 600\text{ V}$ $V_{GE} = 15\text{ V} \sim -15\text{ V}$ $I_C = 75\text{ A}$	-	108	-	ns
t_r	Rise Time		-	48	-	ns
$t_d(off)$	Turn-Off Delay Time		-	580	-	ns
t_f	Fall Time		-	265	-	ns
E_{on}	Turn-On Switching Loss		-	10.1	-	mJ
E_{off}	Turn-Off Switching Loss		-	6.5	-	mJ
E_{is}	Total Switching Loss		-	16.6	-	mJ
Q_g	Total Gate Charge	$V_{GE} = 0\text{ V} \sim +15\text{ V}$	-	470	-	nC
Q_{ge}	Gate-Emitter Charge		-	58	-	nC
Q_{gc}	Gate-Collector Charge		-	290	-	nC

Electrical Characteristics of Diode $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units	
V_F	Diode Forward Voltage	$I_F = 75\text{ A}, V_{GE} = 0\text{ V}$	$T_C = 25^\circ\text{C}$	-	1.6	-	V
			$T_C = 125^\circ\text{C}$	-	1.5	-	
t_{rr}	Diode Reverse Recovery Time	$R_G = 15\ \Omega$ $L = 100\ \mu\text{H}$ $V_{DC} = 600\text{ V}$ $V_{GE} = 15\text{ V} \sim -15\text{ V}$ $I_C = 75\text{ A}$	$T_C = 25^\circ\text{C}$	-	465	-	ns
			$T_C = 125^\circ\text{C}$	-	697	-	
I_{RRM}	Diode Peak Reverse Recovery Current		$T_C = 25^\circ\text{C}$	-	80	-	A
			$T_C = 125^\circ\text{C}$	-	114	-	
Q_{rr}	Diode Reverse Recovery Charge		$T_C = 25^\circ\text{C}$	-	9.7	-	μC
			$T_C = 125^\circ\text{C}$	-	19.9	-	
E_{rr}	Diode Reverse Recovery Energy	$T_C = 25^\circ\text{C}$	-	3.0	-	mJ	
		$T_C = 125^\circ\text{C}$	-	6.9	-		

Thermal Characteristics

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
$R_{th(J-C)}$	Thermal Resistance (IGBT Part)	Junction-to-Case	-	0.24	-	$^\circ\text{C/W}$
$R_{th(J-C)D}$	Thermal Resistance (Diode Part)	Junction-to-Case	-	0.48	-	$^\circ\text{C/W}$

* This specifications may not be considered as an assurance of characteristics and may not have same characteristics in case of using different test systems from @LSIS. We therefore strongly recommend prior consultation of our engineers.

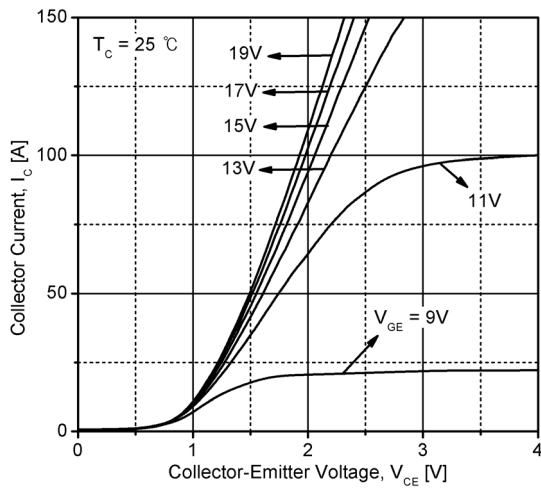


Fig 1. Typical IGBT Output Characteristics

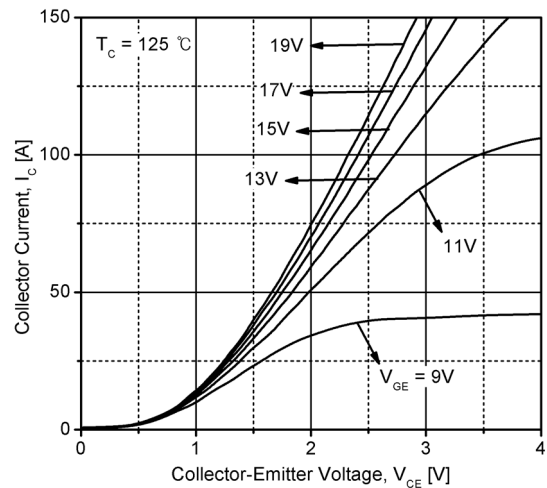


Fig 2. Typical IGBT Output Characteristics

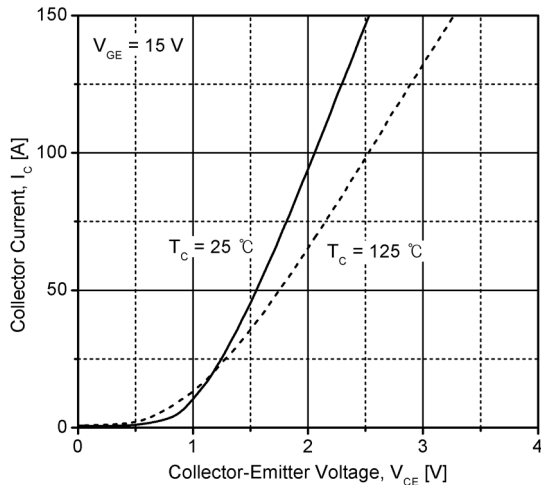


Fig 3. Typical IGBT Output Characteristics

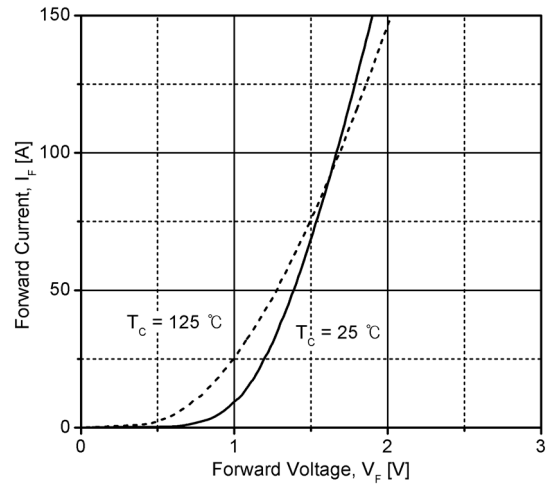


Fig 4. Typical Diode Forward Characteristics

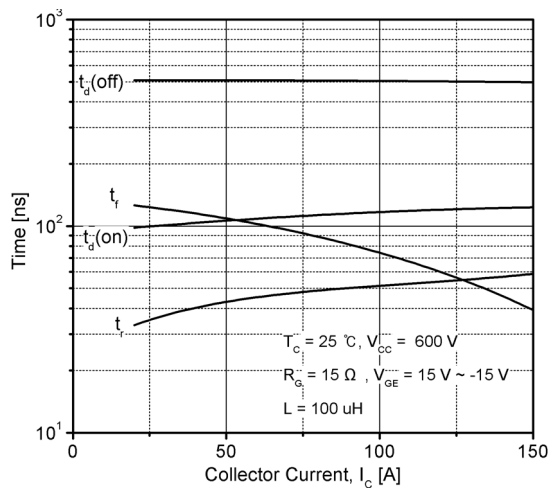


Fig 5. Typical Switching Time vs. Collector Current

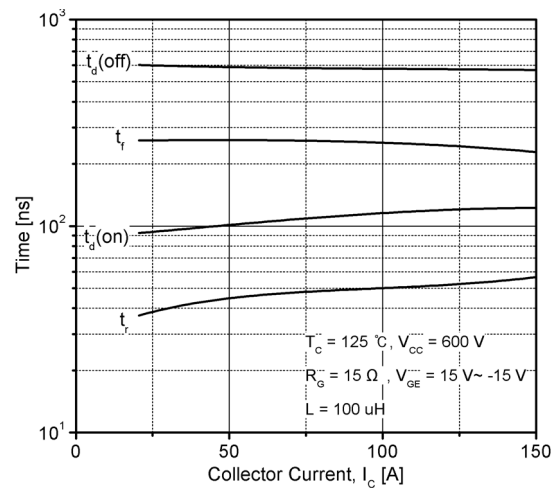


Fig 6. Typical Switching Time vs. Collector Current

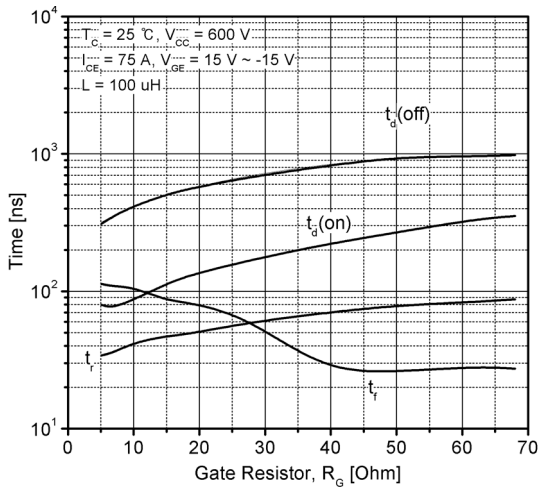


Fig 7. Typical Switching Time vs. Gate Resistor

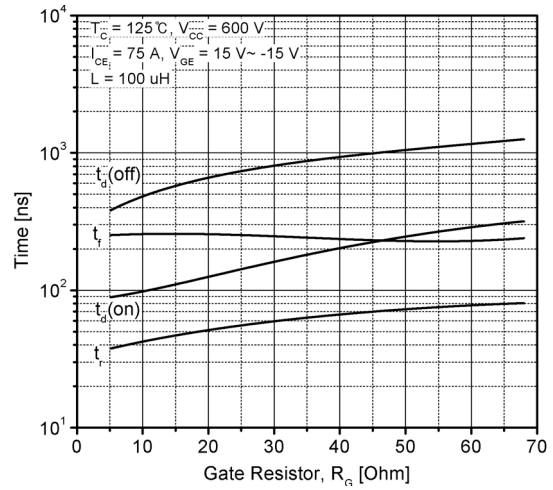


Fig 8. Typical Switching Time vs. Gate Resistor

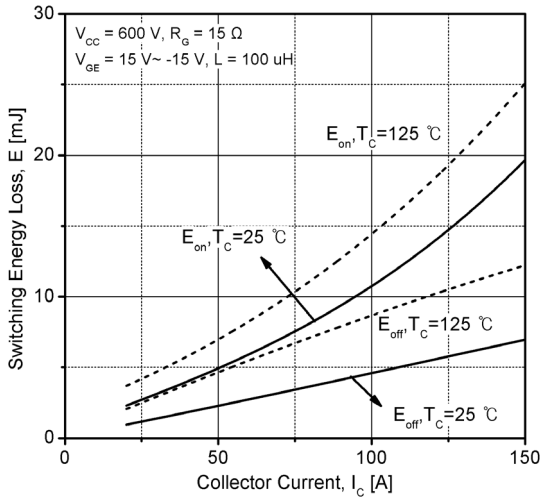


Fig 9. Typical IGBT Switching Loss

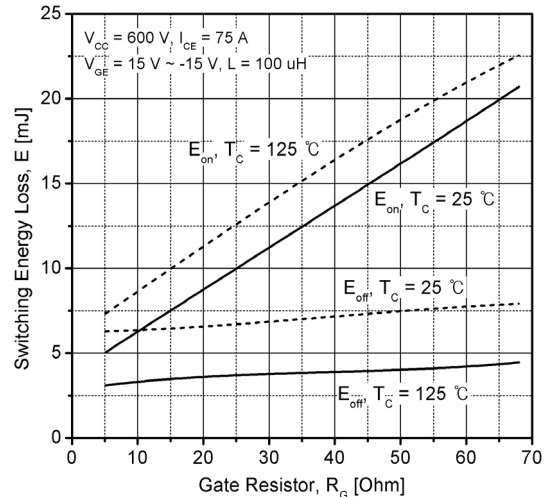


Fig 10. Typical IGBT Switching Loss

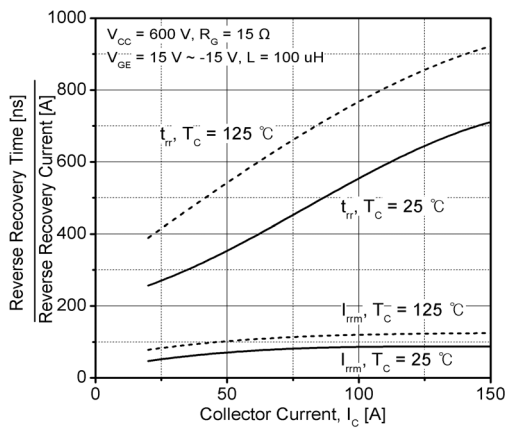


Fig 11. Typical Recovery Characteristics of Diode

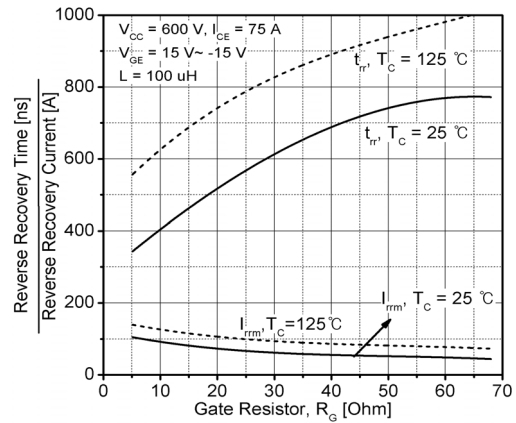


Fig 12. Typical Recovery Characteristics of Diode

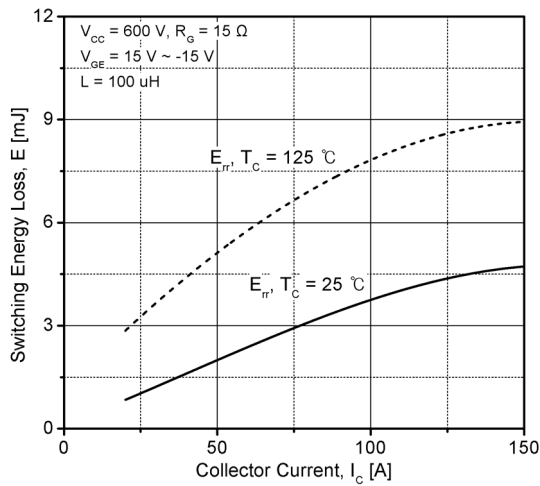


Fig 13. Typical Diode Switching Loss

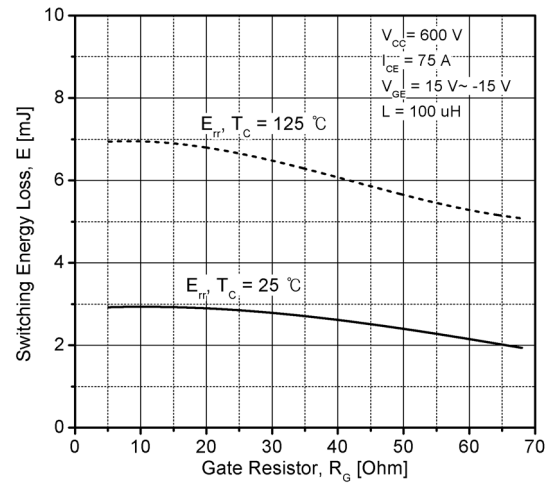


Fig 14. Typical Diode Switching Loss

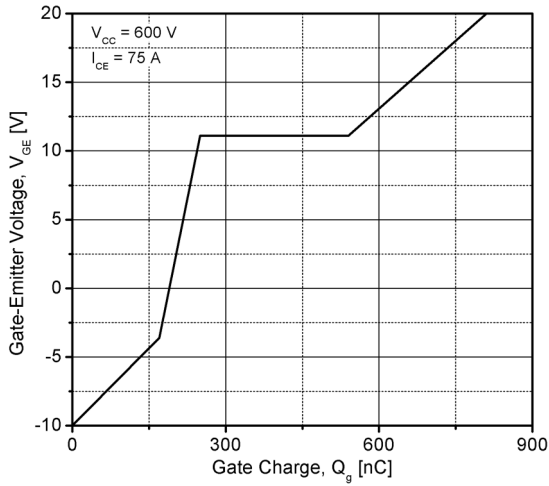


Fig 15. Typical Gate Charge Characteristics

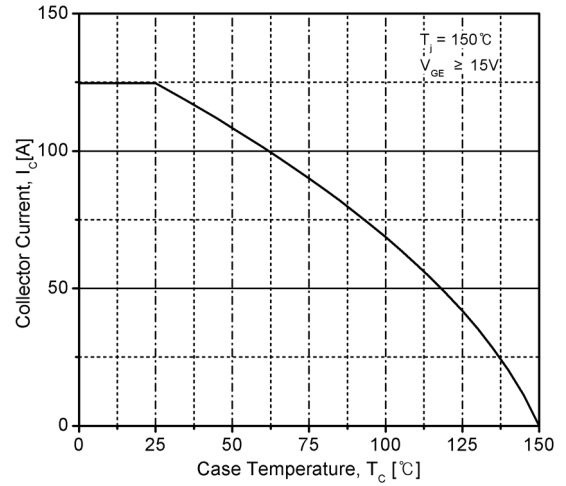


Fig 16. Case Temperature vs. Collector Current

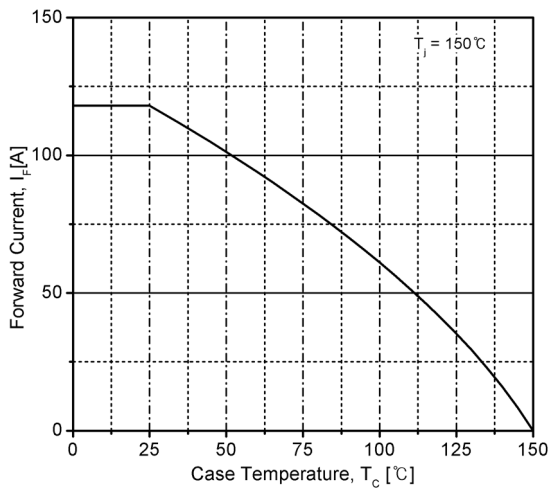


Fig 17. Case Temperature vs. Diode Current

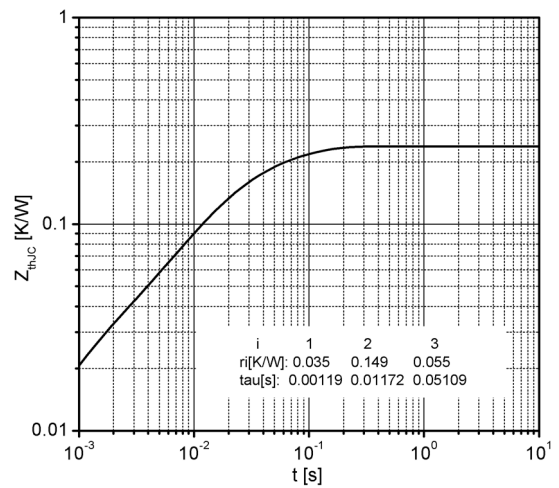


Fig 18. Typical IGBT Thermal Impedance

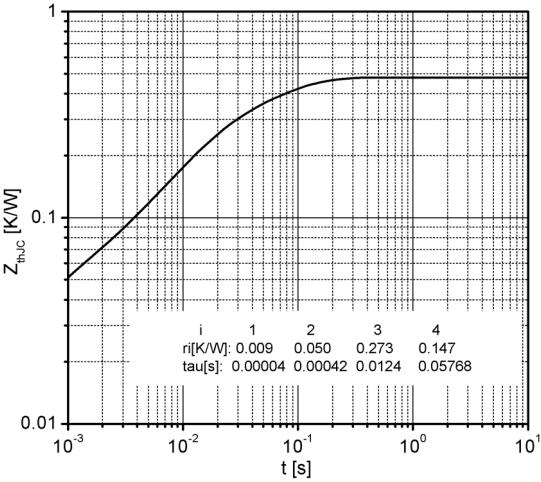
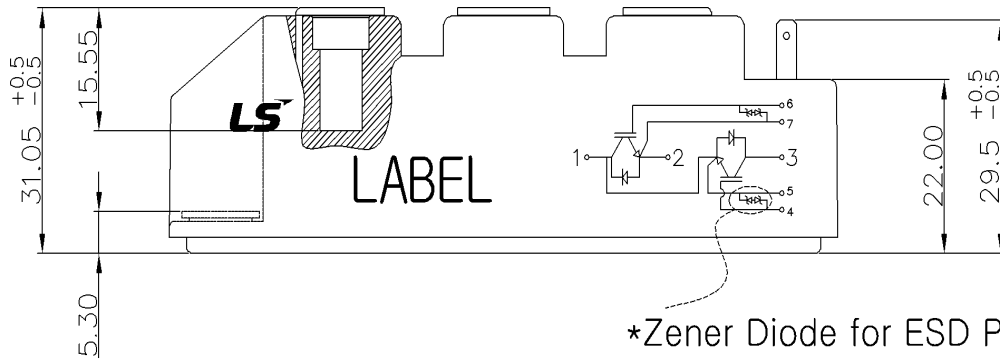
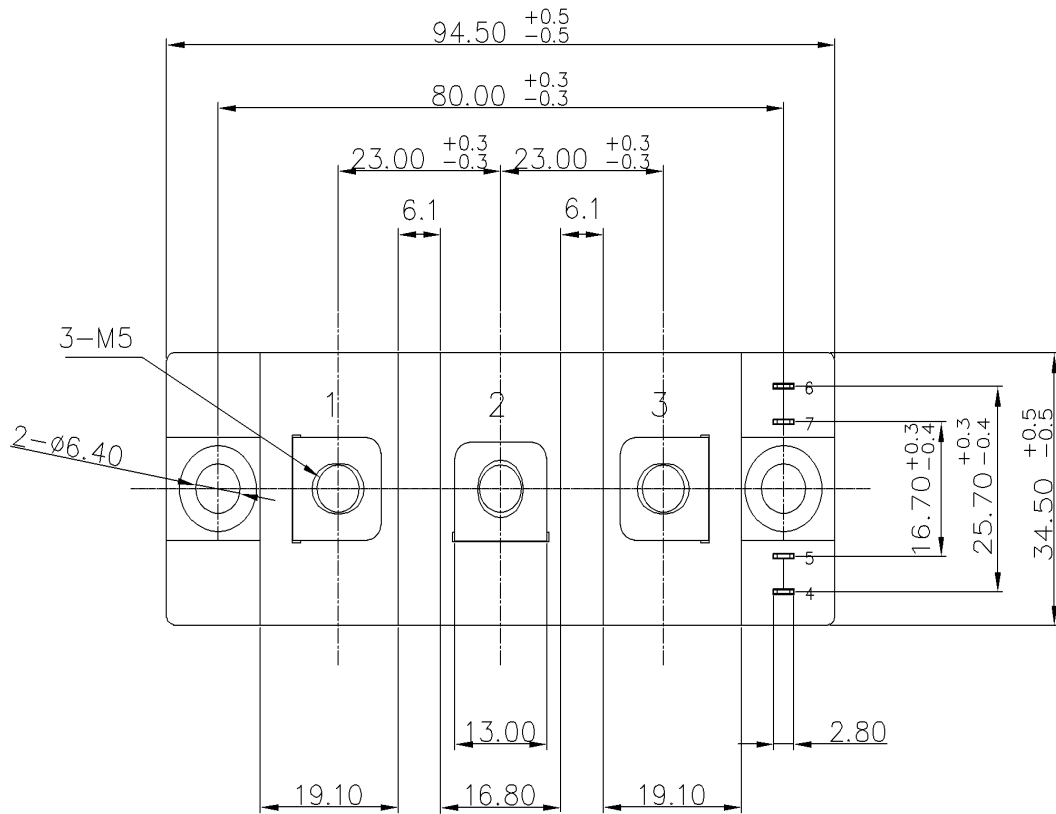


Fig 19. Typical Diode Thermal Impedance

LUH75G1201Z*(1)

Package Dimension(Dimension in mm)



*Zener Diode for ESD Protecton.