

Features

- Trench & Field Stop technology
 - Low saturation voltage
 - Low turn-off losses
 - Positive temperature coefficient
- Free wheeling diodes with fast and soft reverse recovery
- Industrial standard package with copper base plate

Applications

- Welder / Power supply
- UPS / Inverter
- Industrial motor driver

Preliminary data

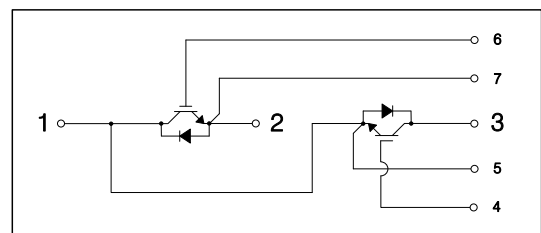


Absolute Maximum Ratings $T_j = 25^\circ\text{C}$ unless otherwise noted

Item	Symbol	Conditions	Value	Units
IGBT	V_{CES}		1200	V
	V_{GES}		± 20	V
	I_C	@ $T_j = 175^\circ\text{C}$, $T_C = 25^\circ\text{C}$, Continuous	325	A
		@ $T_j = 175^\circ\text{C}$, $T_C = 80^\circ\text{C}$, Continuous	245	A
	I_{CM}	@ $T_C = 80^\circ\text{C}$, $t_p = 1\text{ ms}$	490	A
	T_j	Operating Junction Temperature ⁽¹⁾	-40~125	$^\circ\text{C}$
P_D	@ $T_j = 175^\circ\text{C}$, $T_C = 25^\circ\text{C}$	1250	W	
	@ $T_j = 175^\circ\text{C}$, $T_C = 80^\circ\text{C}$	750	W	
Diode	V_{RRM}		1200	V
	I_F		200	A
	I_{FRM}	$t_p = 1\text{ ms}$	400	A
	T_j	Operating Junction Temperature ⁽¹⁾	-40~125	$^\circ\text{C}$
Module	T_{stg}	Storage Temperature	-40~125	$^\circ\text{C}$
	V_{iso}	@ AC 1 minute	2500	V
	M_t	Main Terminal Mounting Torque (M6)	2.5~6.0	Nm
	M_S	Heat Sink Mounting Torque (M6)	3.0~6.0	Nm
	W	Weight	260	g

Internal Circuit & Pin Description

Pin Number	Pin Name	Pin Description
1	C2E1	Output
2	E2	Negative DC Link Output
3	C1	Positive DC Link Output
4	G1	Gate Input for High-side
5	E1	Emitter Input for High-side
6	G2	Gate Input for Low-side
7	E2	Emitter Input for Low-side



(Note *1) The Maximum junction temperature of chip is 175 $^\circ\text{C}$.

Electrical Characteristics of IGBT and Diode $T_j = 25^\circ\text{C}$ unless otherwise noted

Static Characteristics

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
BV_{CES}	C-E Breakdown Voltage	$V_{GE} = 0\text{ V}, I_C = 1\text{ mA}$	1200	-	-	V
I_{CES}	C-E Cut-Off Current	$V_{CE} = V_{CES}, V_{GE} = 0\text{ V}$	-	-	1	mA
I_{GES}	G-E Leakage Current	$V_{GE} = V_{GES}, V_{CE} = 0\text{ V}$	-	-	-	nA
$V_{GE(th)}$	G-E Threshold Voltage	$V_{GE} = V_{CE}, I_C = 200\text{ mA}$	-	6.9	-	V
$V_{CE(sat)}$	Collector to Emitter Saturation Voltage(Chip level)	$I_C = 200\text{ A}, V_{GE} = 15\text{ V}, T_j = 25^\circ\text{C}$	-	1.9	2.3	V
		$I_C = 200\text{ A}, V_{GE} = 15\text{ V}, T_j = 175^\circ\text{C}$	-	2.5	-	V
	Collector to Emitter Saturation Voltage	$I_C = 200\text{ A}, V_{GE} = 15\text{ V}, T_j = 25^\circ\text{C}$	-	2.2	-	V
		$I_C = 200\text{ A}, V_{GE} = 15\text{ V}, T_j = 125^\circ\text{C}$	-	2.7	-	V

Dynamic Characteristics

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
I_{SC}	Short Current	$V_{GE} \leq 15\text{ V}, V_{CC} = 800\text{ V}$ $V_{CE} \leq V_{CES}, T_j = 125, T_P \leq 10\mu\text{s}$		1000		A
C_{ies}	Input Capacitance	$V_{CE} = 25\text{ V}, V_{GE} = 0\text{ V}$ $f = 1\text{ MHz}, T_j = 25^\circ\text{C}$	-	19.2	-	nF
C_{oes}	Output Capacitance		-	1.4	-	nF
C_{res}	Reverse Transfer Capacitance		-	0.7	-	nF
$t_d(on)$	Turn-On Delay Time	$T_j = 125^\circ\text{C}, R_G = 5.1\ \Omega$ $L = 100\ \mu\text{H}, V_{DC} = 600\text{ V}$ $V_{GE} = 15\text{ V} \sim -15\text{ V}$ $I_C = 200\text{ A}$	-	194	-	ns
t_r	Rise Time		-	98	-	ns
$t_d(off)$	Turn-Off Delay Time		-	489	-	ns
t_f	Fall Time		-	212	-	ns
E_{on}	Turn-On Switching Loss		-	24.6	-	mJ
E_{off}	Turn-Off Switching Loss		-	14.5	-	mJ
E_{ts}	Total Switching Loss		-	39.1	-	mJ
Q_g	Total Gate Charge	$V_{GE} = 0\text{ V} \sim +15\text{ V}$	-	1060	-	nC
Q_{ge}	Gate-Emitter Charge		-	260	-	nC
Q_{gc}	Gate-Collector Charge		-	540	-	nC

Electrical Characteristics of Diode

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units	
V_F	Diode Forward Voltage	$I_F = 200\text{ A}$ $V_{GE} = 0\text{ V}$	$T_j = 25^\circ\text{C}$	-	2.3	-	V
			$T_j = 125^\circ\text{C}$	-	2.3	-	
t_{rr}	Diode Reverse Recovery Time	$R_G = 5.1\ \Omega$ $L = 100\ \mu\text{H}$ $V_{DC} = 600\text{ V}$ $V_{GE} = 15\text{ V} \sim -15\text{ V}$ $I_C = 200\text{ A}$	$T_j = 25^\circ\text{C}$	-	266	-	ns
			$T_j = 125^\circ\text{C}$	-	490	-	
I_{RRM}	Diode Peak Reverse Recovery Current		$T_j = 25^\circ\text{C}$	-	103	-	A
			$T_j = 125^\circ\text{C}$	-	147	-	
Q_{rr}	Diode Reverse Recovery Charge		$T_j = 25^\circ\text{C}$	-	6.0	-	μC
			$T_j = 125^\circ\text{C}$	-	23.5	-	
E_{rr}	Diode Reverse Recovery Energy		$T_j = 25^\circ\text{C}$	-	0.7	-	mJ
		$T_j = 125^\circ\text{C}$	-	6.9	-		

Thermal Characteristics

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
$R_{th(J-C)}$	Thermal Resistance (IGBT Part)	Junction-to-Case	-	0.12	-	$^\circ\text{C/W}$
$R_{th(J-C)D}$	Thermal Resistance (Diode Part)	Junction-to-Case	-	0.26	-	$^\circ\text{C/W}$
$R_{th(C-H)}$	Thermal Resistance (IGBT Part)	Case-to-Heatsink	-	-	-	$^\circ\text{C/W}$
$R_{th(C-H)D}$	Thermal Resistance (Diode Part)	Case-to-Heatsink	-	-	-	$^\circ\text{C/W}$

* This specifications may not be considered as an assurance of characteristics and may not have same characteristics in case of using different test systems from @ LSIS. We therefore strongly recommend prior consultation of our engineers.

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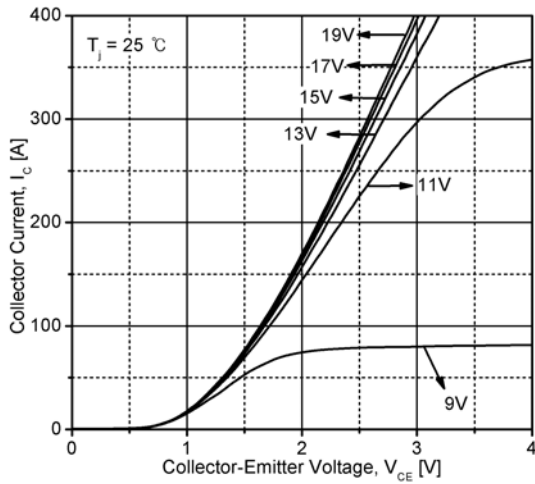


Fig 1. Typical IGBT Output Characteristics

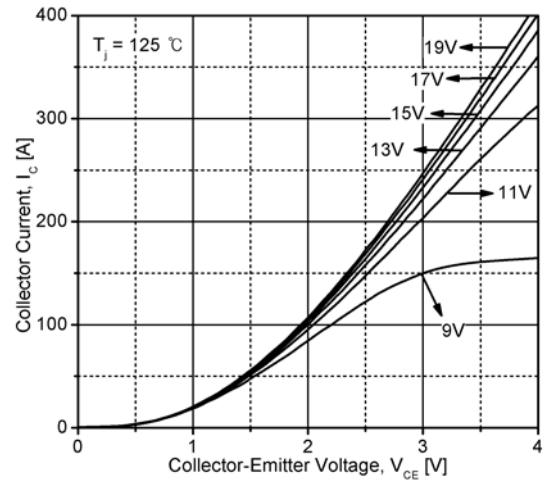


Fig 2. Typical IGBT Output Characteristics

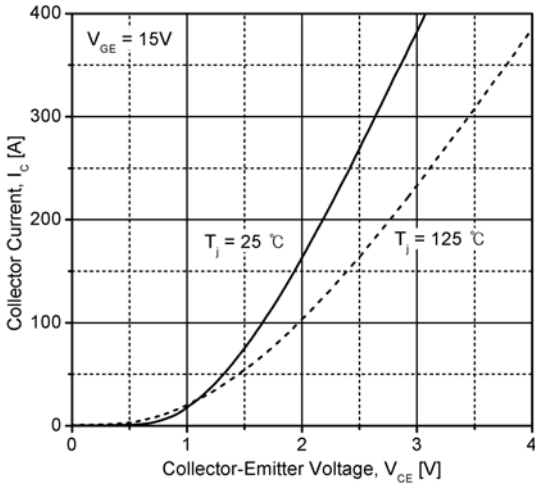


Fig 3. Typical IGBT Output Characteristics

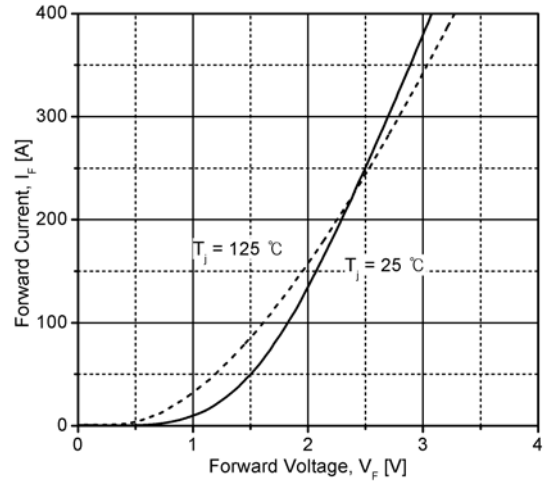


Fig 4. Typical Diode Forward Characteristics

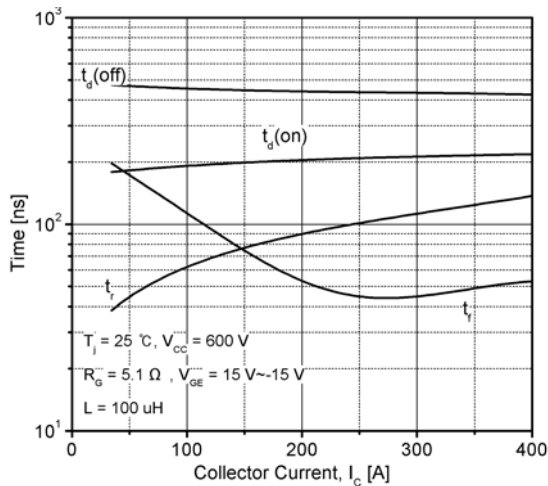


Fig 5. Typical Switching Time vs. Collector Current

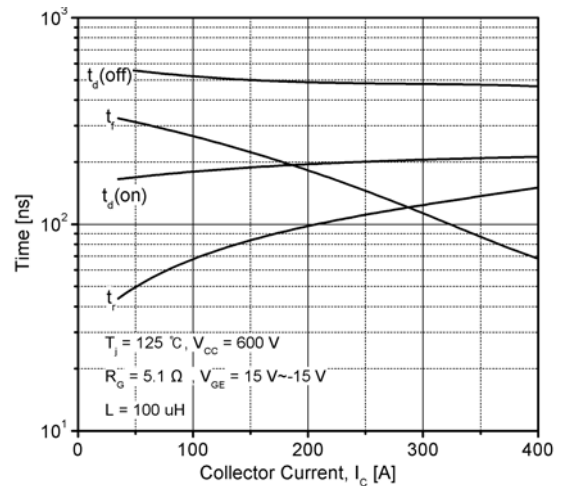


Fig 6. Typical Switching Time vs. Collector Current

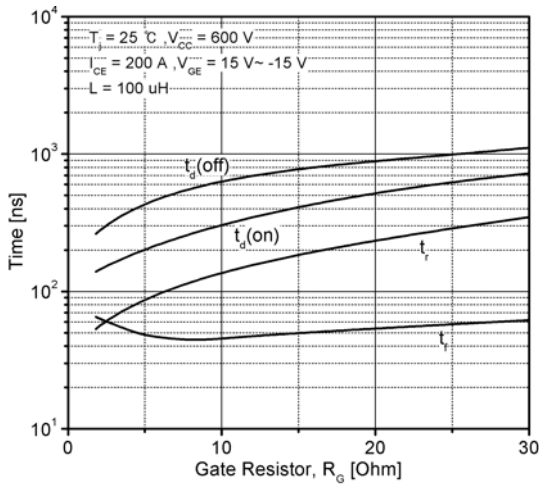


Fig 7. Typical Switching Time vs. Gate Resistor

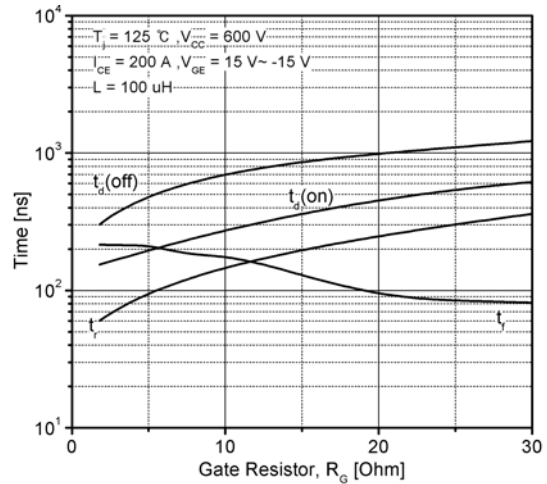


Fig 8. Typical Switching Time vs. Gate Resistor

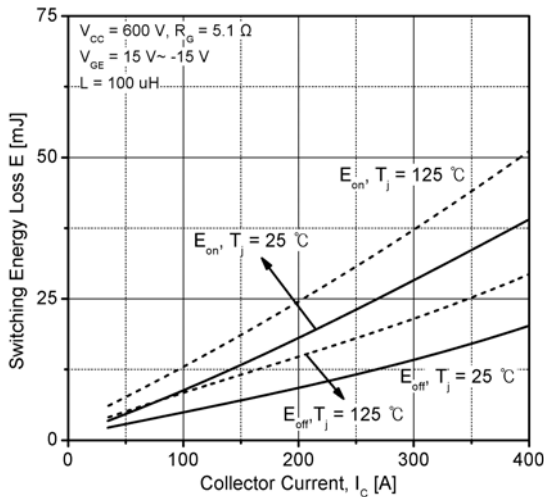


Fig 9. Typical IGBT Switching Loss

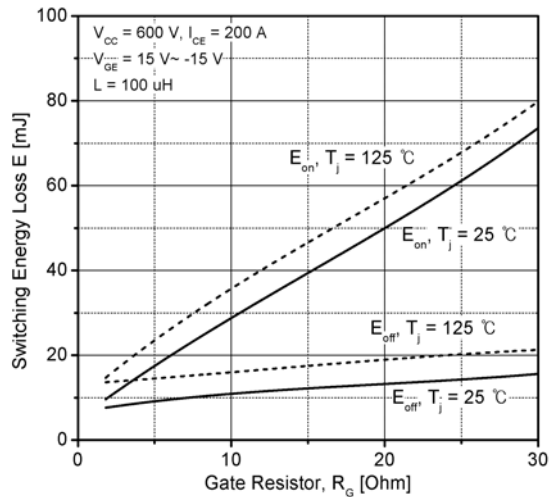


Fig 10. Typical IGBT Switching Loss

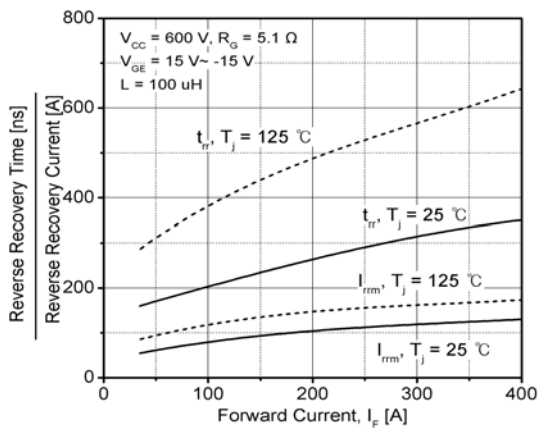


Fig 11. Typical Recovery Characteristics of Diode

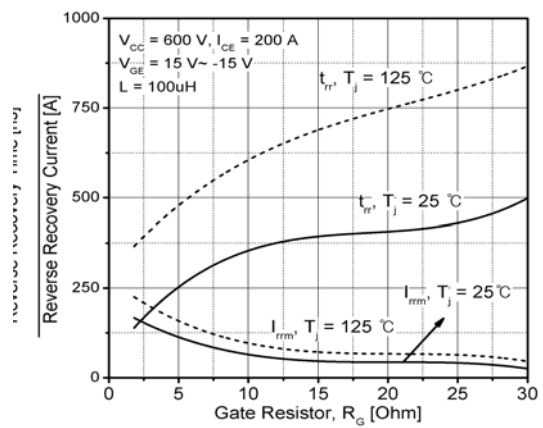


Fig 12. Typical Recovery Characteristics of Diode

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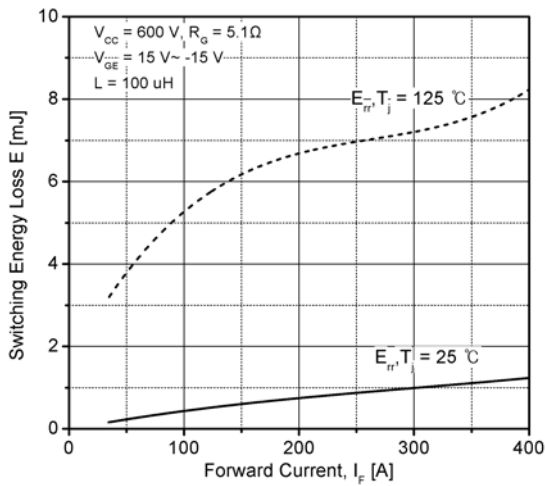


Fig 13. Typical Diode Switching Loss

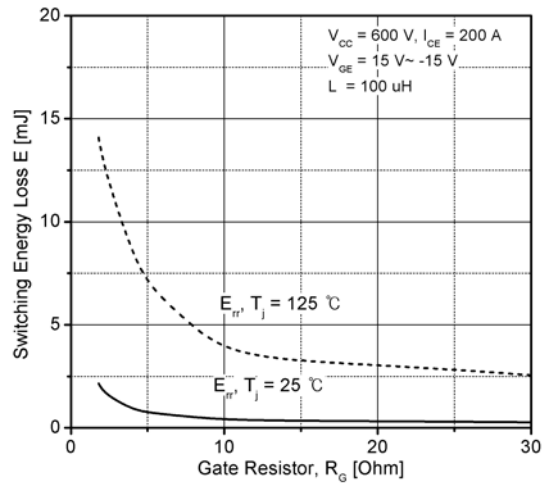


Fig 14. Typical Diode Switching Loss

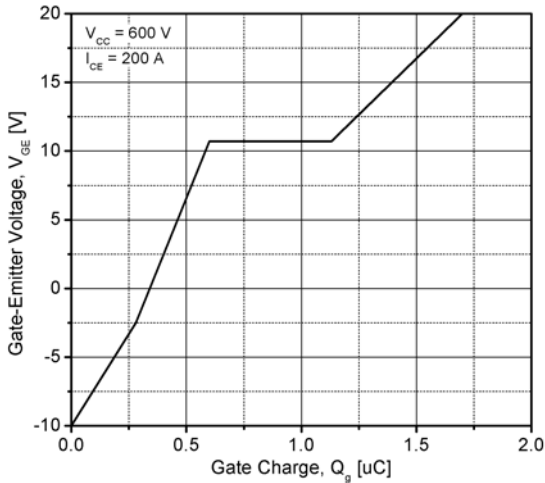


Fig 15. Typical Gate Charge Characteristics

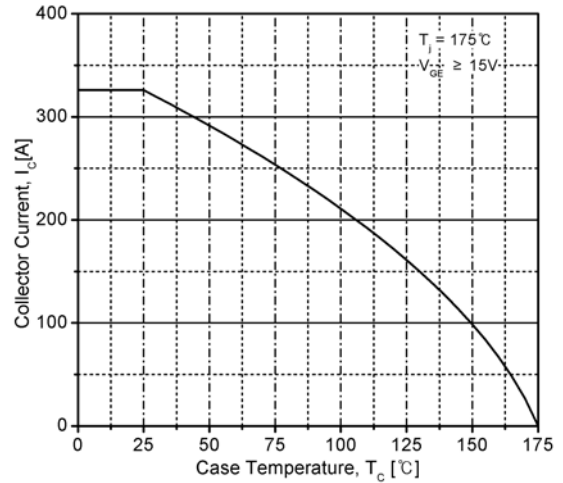


Fig 16. Case Temperature vs. Collector Current

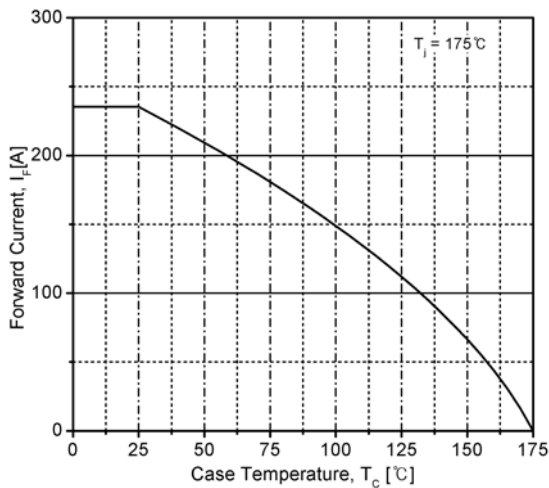


Fig 17. Case Temperature vs. Diode Current

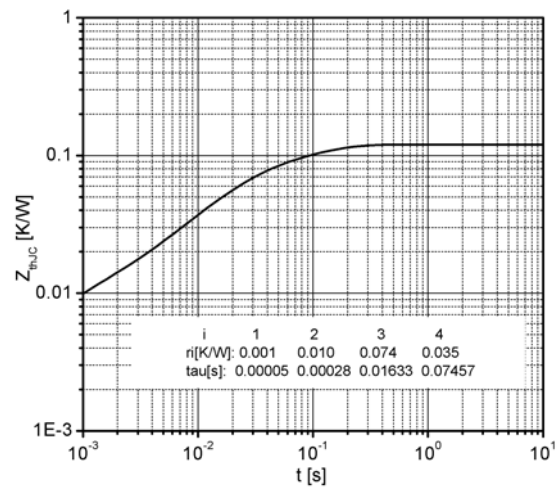


Fig 18. Typical IGBT Thermal Impedance

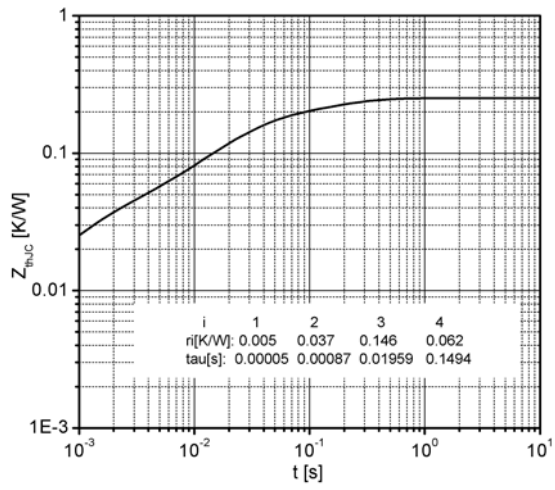


Fig 19. Typical Diode Thermal Impedance

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Package Dimension (Dimension in mm)

